

Model Name: H310M H

SHEET

TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1151-A
05	CPU_LGA1151-B-DDR4
06	CPU_LGA1151-C
07	CPU_LGA1150-D
08	DDR4 CHANNEL A
09	DDR4 CHANNEL B
10	PCH_CLK BUFFER
11	PCH_DMI,USB,PCIE
12	PCH_MISC
13	PCH SATA,PCIE,SATA_EXPRESS
14	PCH_PWR
15	PCH_GND
16	ITE 8686 LPC IO
17	BIOS
18	FAN CTRL--SIO
19	HWM
20	PCI EXPRESS*16 SLOT
21	PCI EXPRESS*1 SLOT
22	SATA
23	ISL95858_856 PWM
24	ISL95858_856 MOS_VCORE
25	ISL95858_856 MOS_VCCGT
26	VCCSA_VCCIO_VCCPLL
27	RT8237_DDR_VDDQ

rev:1.01

SHEET

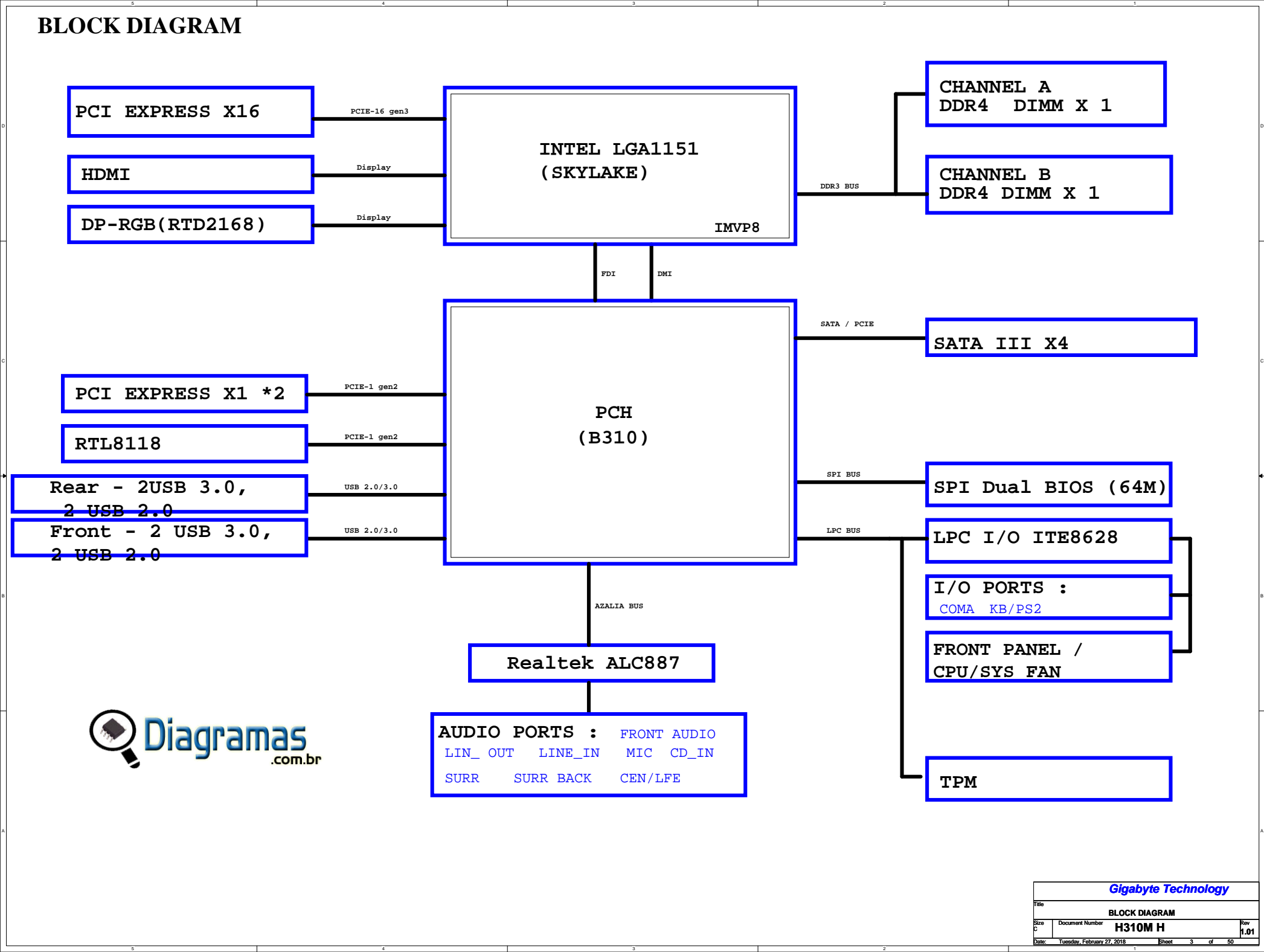
TITLE

28	RT8068_VPP
29	RT8237_PCH_VCC1_0_PCH
30	DISCRETE POWER
31	ATX POWER , A_-PROCHOT
32	KB_MS_USB
33	RTD2168 - DP to VGA - IC
34	RTD2168 - DP to VGA - Conn
35	R_USB30
36	REALTEK - RTL8111G
37	USB20_LAN CONNECTOR-RTL8111G
38	Realtek ALC887
39	REAR AUDIO JACK
40	F_USB30
41	F_USB20
42	M.2 X2
43	COM , LPT , TPM
44	F_PANEL
45	HDMI CONN
46	AUDIO LED
47	EMI/ESD
48	POWER MAP
49	TABLE LIST
50	NTC MAP
51	



Gigabyte Technology			
Title			
Cover Sheet			
Size	Document Number	Rev	
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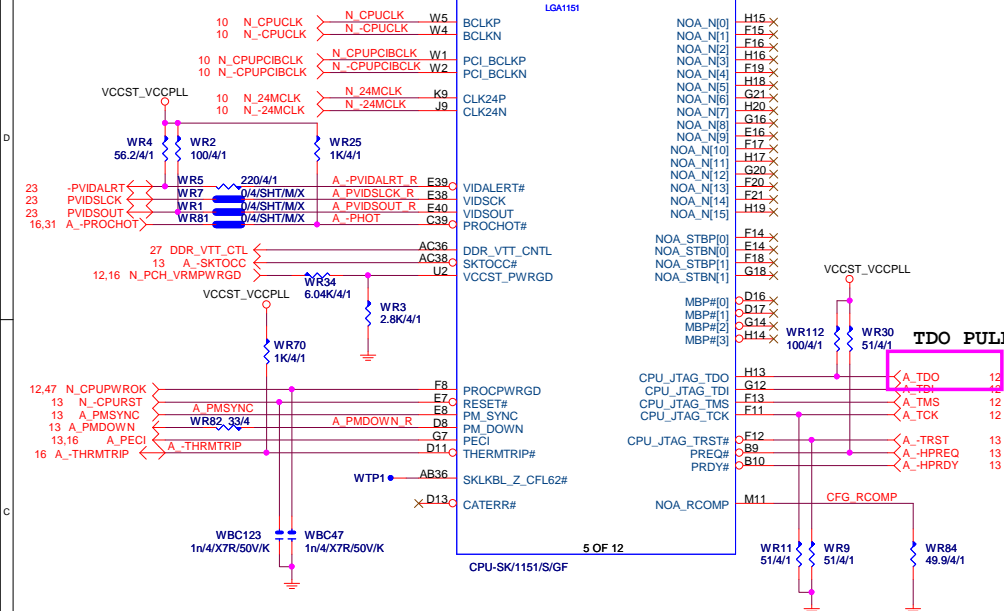
BLOCK DIAGRAM



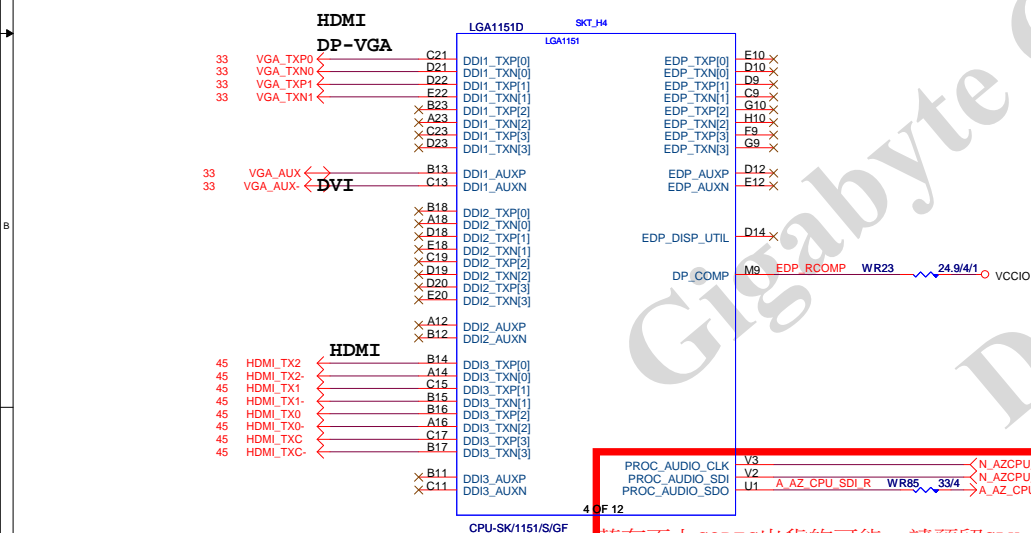
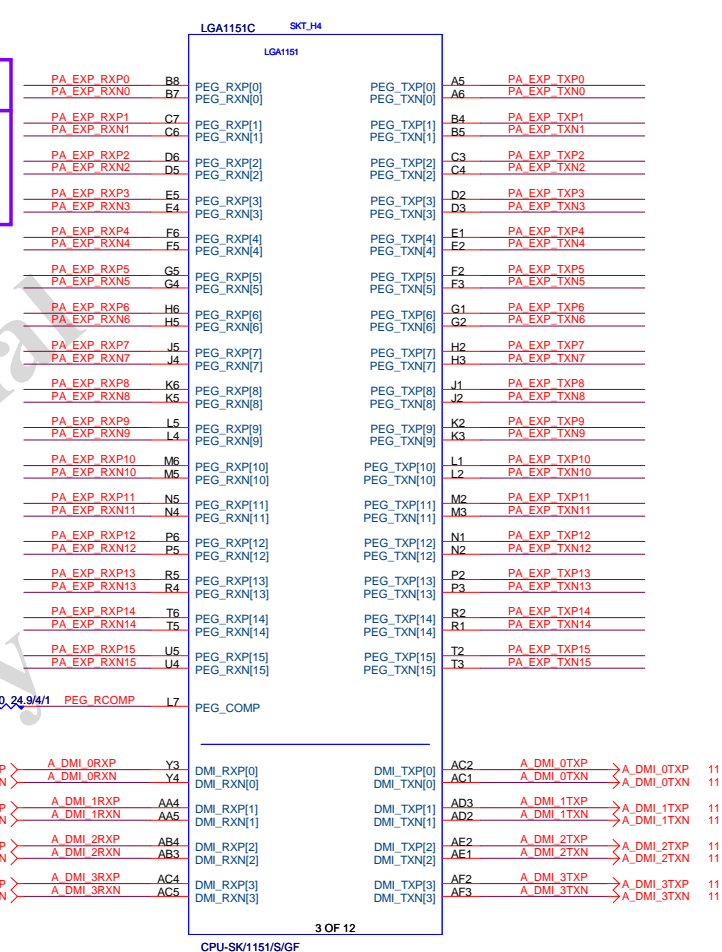
CFL_R0.1

```
CFG[4]: 1=eDP enable / 0=eDP disable
CFG[7]: 1=PEG Train immediately following RESET
        0=PEG Wait for BIOS
```

CFG[13]: 1=VCCSA Fixed Mode / 0=SVID Mode



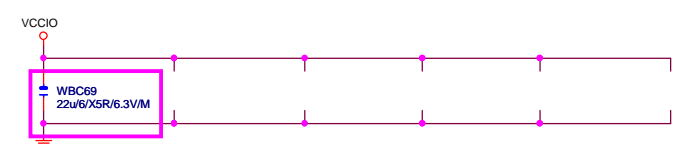
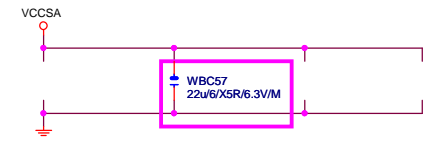
Bifurcation Config.	Signals Lanes		
	CFG[6]	CFG[5]	CFG[2]
1x16	1	1	1
1x16 Reversed	1	1	0
2x8	1	0	1
2x8 Reversed	1	0	0
1x8+2x4	0	0	1
1x8+2x4 Reversed	0	0	0



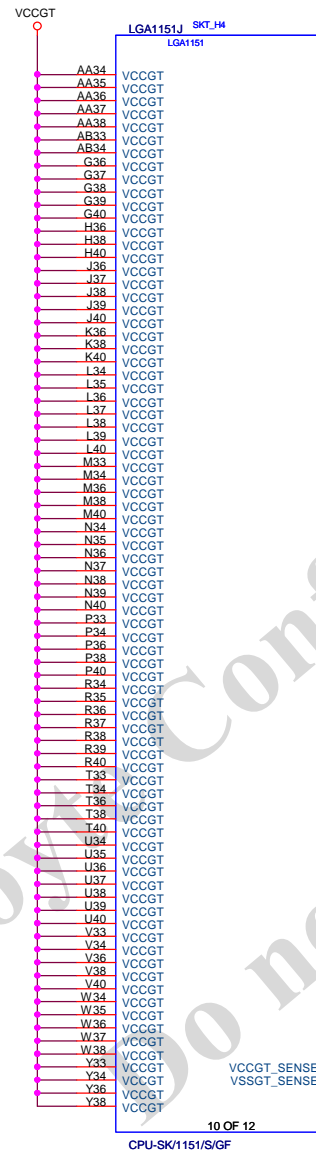
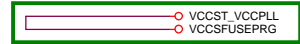
若有不上CODEC出貨的可能，請預留CLK and SDO對地電阻。

G-15u : (CPU-SK/1151/S/15)
10SC1-F01151-11R / 10SC1-F01151-12R
G-FL : (CPU-SK/1151/S/GF)
10SC1-F01151-21R / 10SC1-F01151-22R

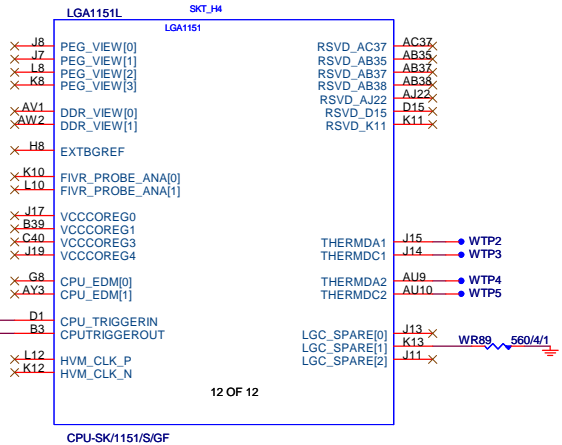
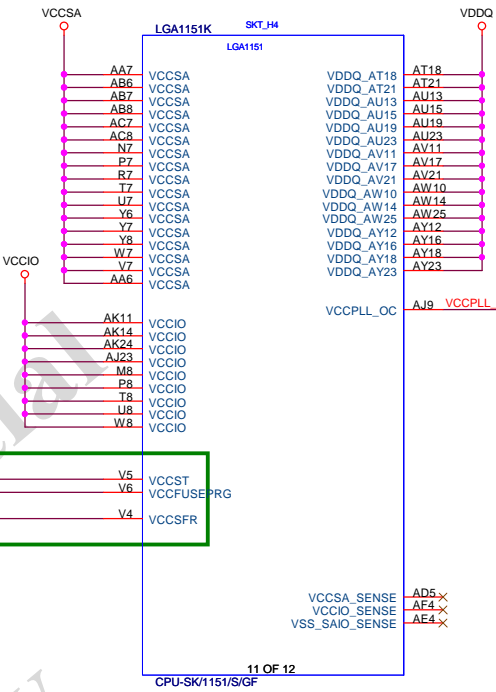
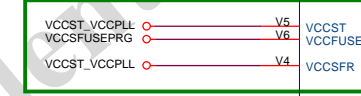
PA_EXP_TXP[0..15]	>>>	PA_EXP_TXP[0..15]	20
PA_EXP_TXN[0..15]	>>>	PA_EXP_TXN[0..15]	20
PA_EXP_RXP[0..15]	>>>	PA_EXP_RXP[0..15]	20
PA_EXP_RXN[0..15]	>>>	PA_EXP_RXN[0..15]	20

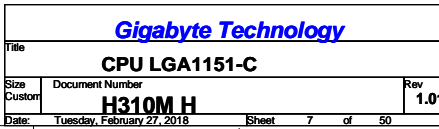


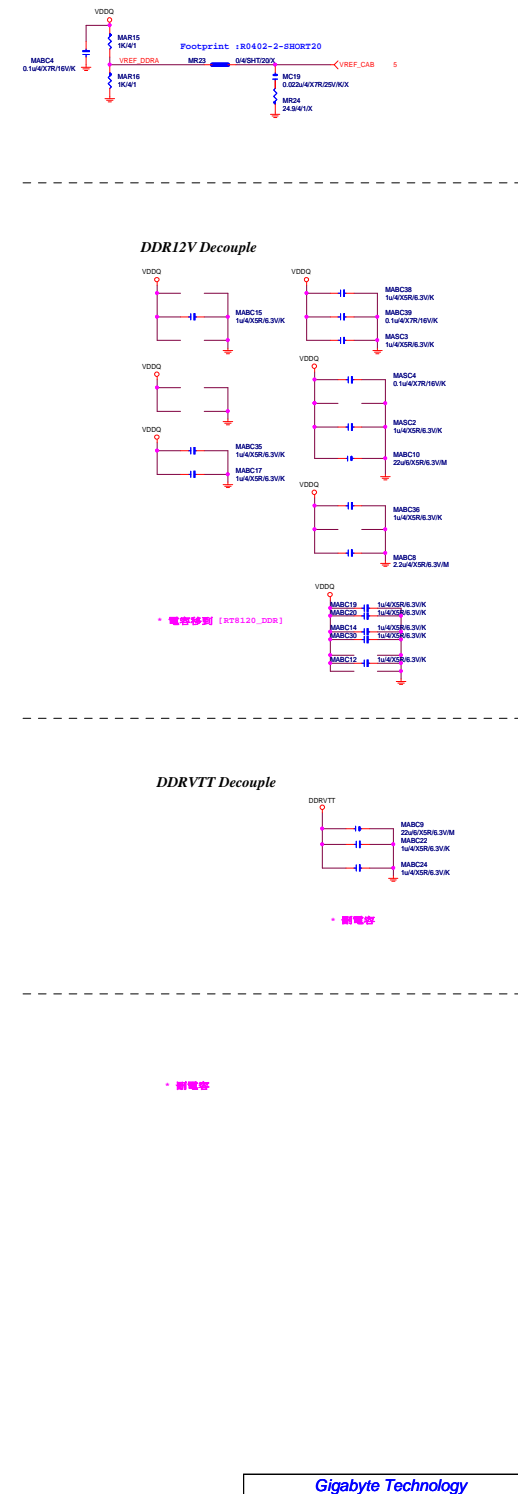
CPU POWER

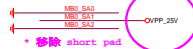
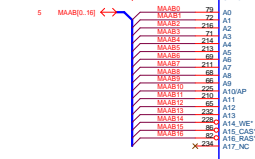
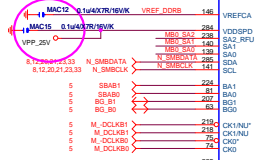
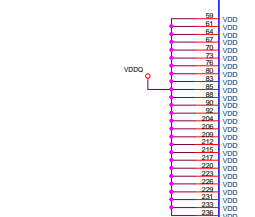
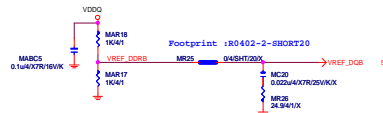


CPU POWER



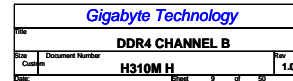




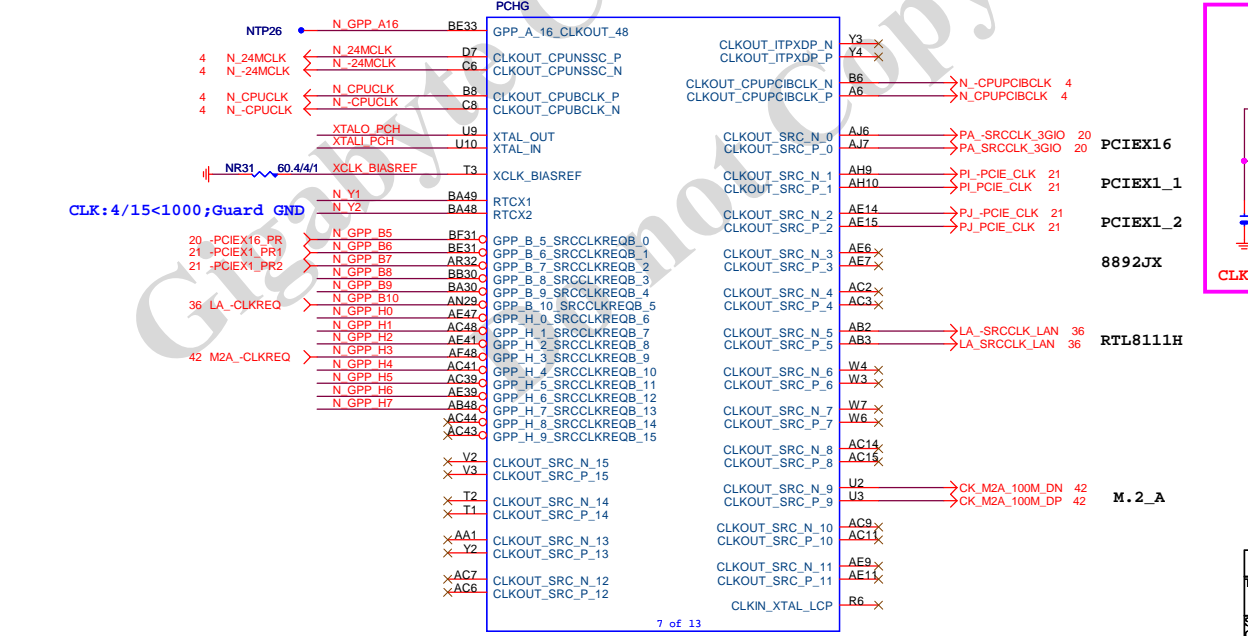
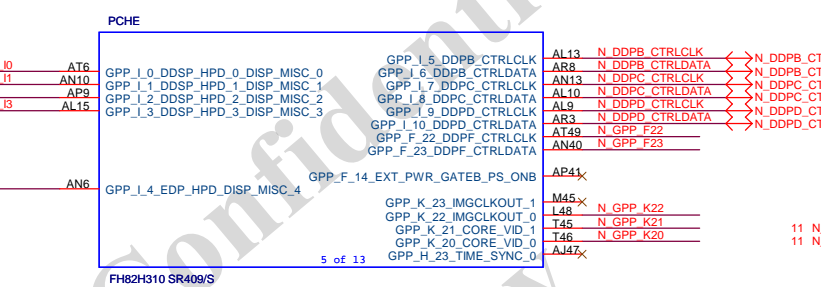
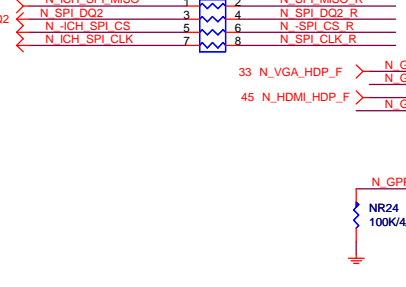
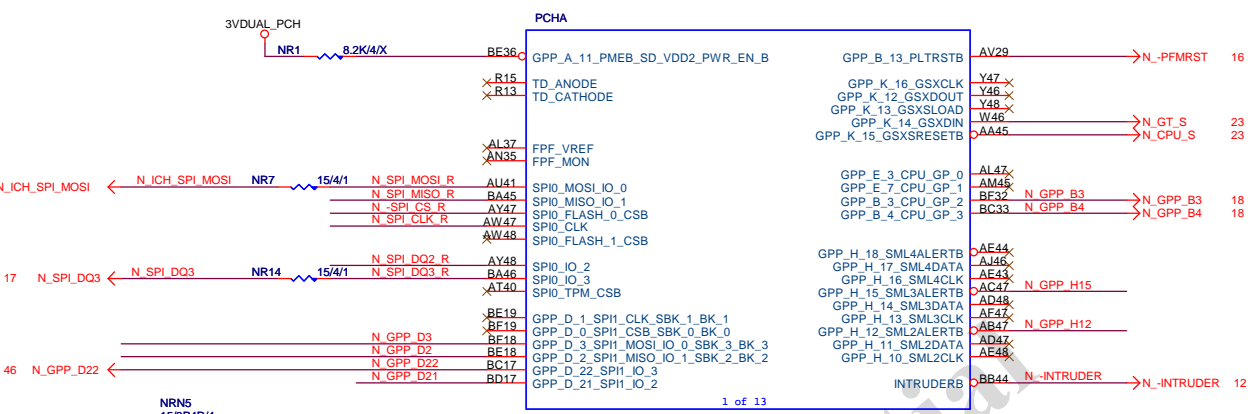
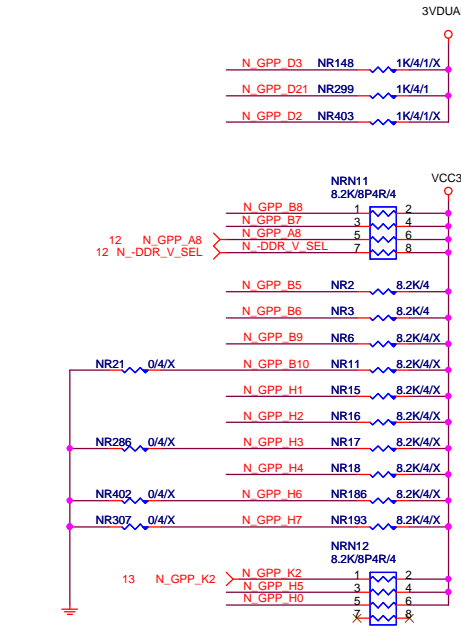
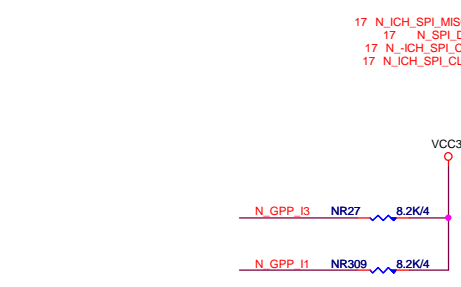
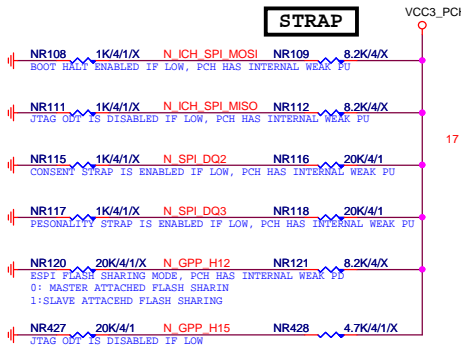


CHANNEL B0
SA2:0=010

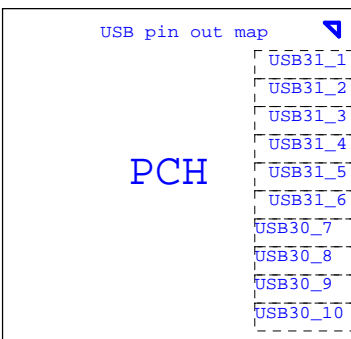
BLACK
* 黑色 雙耳扣



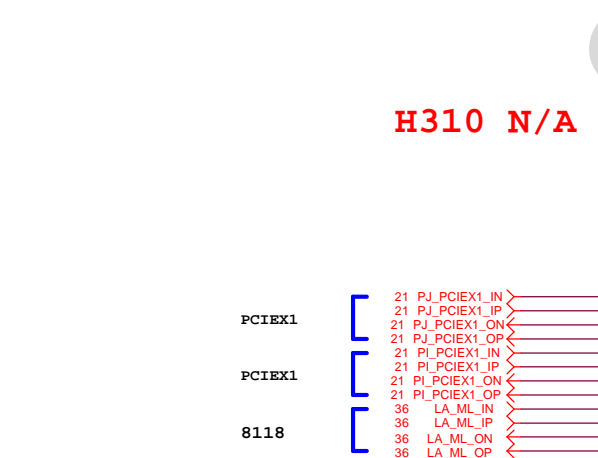
CNP_R0.7



注意左側Table及下方訊號名稱改USB3:

[illegible]

ITEM	USB P7 PCIE P1	USB P8 PCIE P2	USB P9 PCIE P3	USB P10 PCIE P4	PCIE P5	PCIE P6	PCIE P7	PCIE P8
H310	N/A	N/A	N/A	N/A	PCIE GbE	PCIE	PCIE	PCIE
B360	N/A	N/A	N/A	N/A	PCIE GbE	PCIE	PCIE	PCIE
Q360	U3.1 Gen1	U3.1 Gen1	N/A	N/A	PCIE GbE	PCIE	PCIE	PCIE
H370	U3.1 Gen1	U3.1 Gen1	PCIE	PCIE	PCIE GbE	PCIE	PCIE	PCIE
Z390	U3.1 Gen1 PCIE	U3.1 Gen1 PCIE	U3.1 Gen1 PCIE	U3.1 Gen1 PCIE	PCIE GbE	PCIE	PCIE	PCIE
Q370	U3.1 Gen1	U3.1 Gen1	U3.1 Gen1	U3.1 Gen1	PCIE	PCIE	PCIE	PCIE



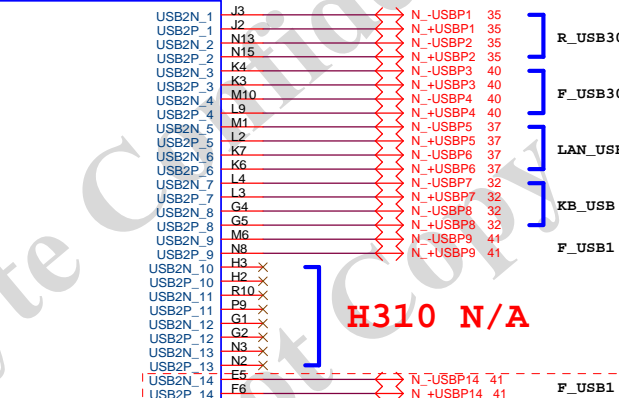
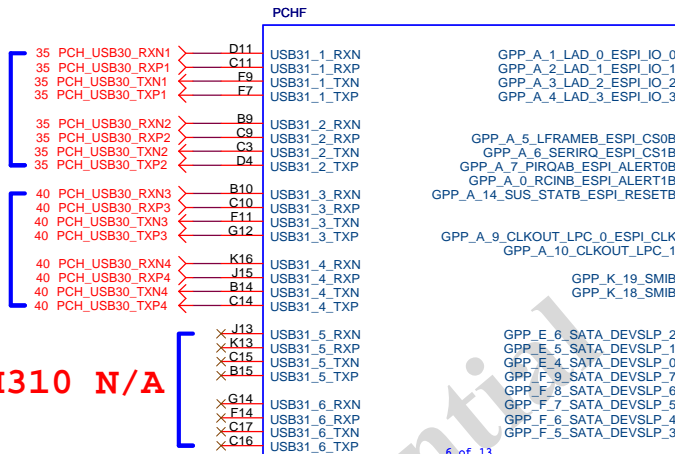
DMI need to reverse			PCHB
4	A DMI 3TXN	A DMI 3TXN	K34
4	A DMI 3TXP	A DMI 3TXP	J35
4	A DMI 3RXN	A DMI 3RXN	C33
4	A DMI 3RXP	A DMI 3RXP	B33
4	A DMI 2TXN	A DMI 2TXN	G33
4	A DMI 2TXP	A DMI 2TXP	F34
4	A DMI 2RXN	A DMI 2RXN	C32
4	A DMI 2RXP	A DMI 2RXP	B32
4	A DMI 1TXN	A DMI 1TXN	K32
4	A DMI 1TXP	A DMI 1TXP	J32
4	A DMI 1RXN	A DMI 1RXN	C31
4	A DMI 1RXP	A DMI 1RXP	B31
4	A DMI 0TXN	A DMI 0TXN	F30
4	A DMI 0TXP	A DMI 0TXP	G30
4	A DMI 0RXN	A DMI 0RXN	C29
4	A DMI 0RXP	A DMI 0RXP	B29
			M29
			D33
			D32
			D31
			D30
			D29
			D28
			D27
			D26
			D25
			D24
			D23
			D22
			D21
			D20
			D19
			D18
			D17
			D16
			D15
			D14
			D13
			D12
			D11
			D10
			D09
			D08
			D07
			D06
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			D04
			D03
			D02
			D01
			D00

R_USB30

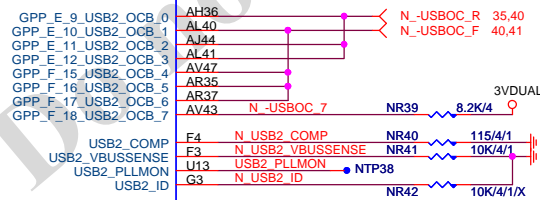
GENI

F_USB30

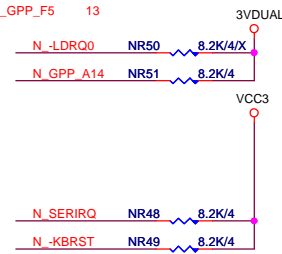
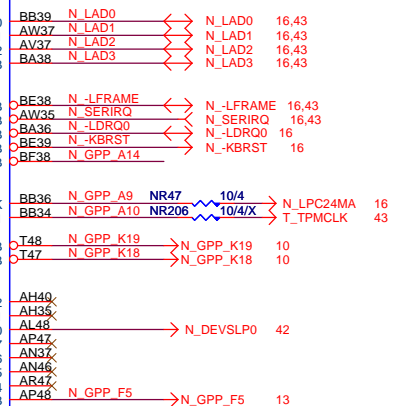
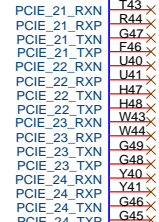
H310 N/A



H310 N/A

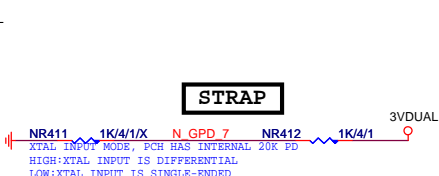


H310 N/A





ITEM	USB P10	USB P11	USB P12	USB P13	USB P14
H310	N/A	N/A	N/A	N/A	USB2
B360	USB2	USB2	N/A	N/A	USB2
Q360	USB2	USB2	USB2	USB2	USB2
H370	USB2	USB2	USB2	USB2	USB2
Z390	USB2	USB2	USB2	USB2	USB2
Q370	USB2	USB2	USB2	USB2	USB2

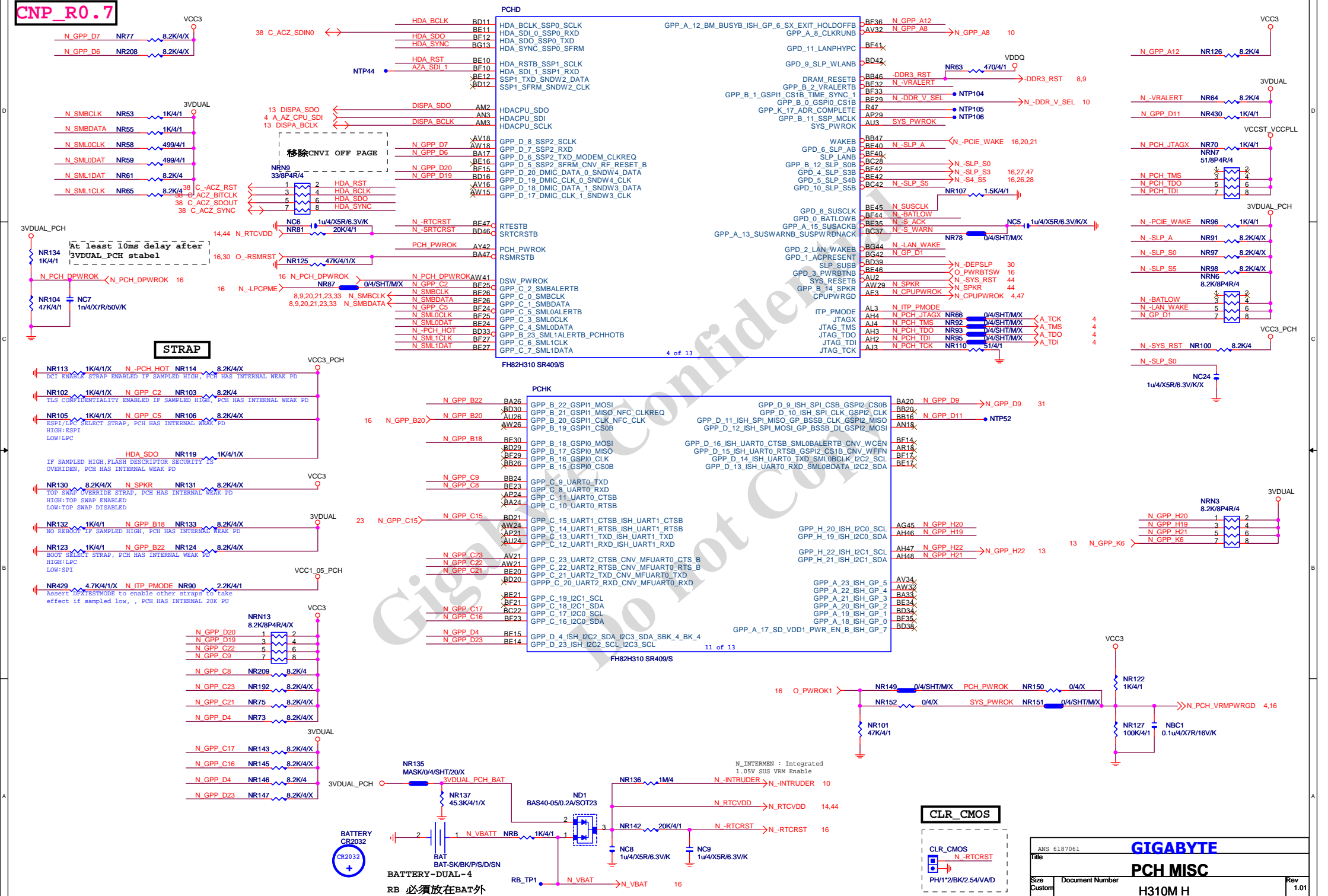
USB1 CNVI的BT功能enable時
PCIE的USB2.0 port14會被disable

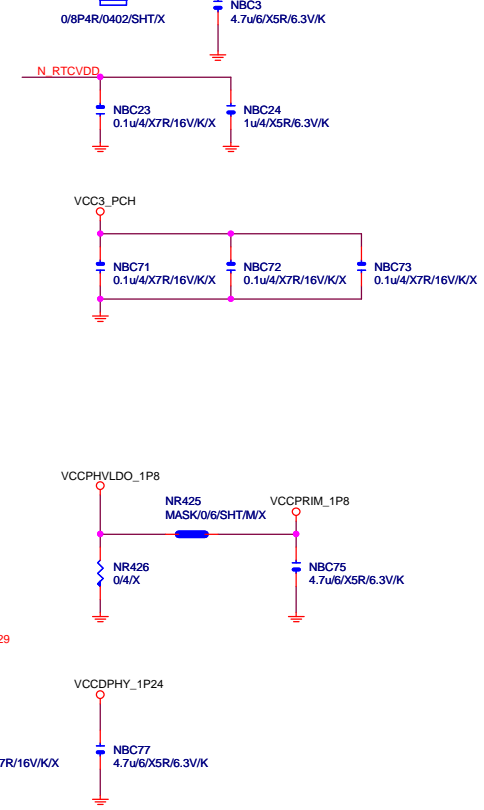
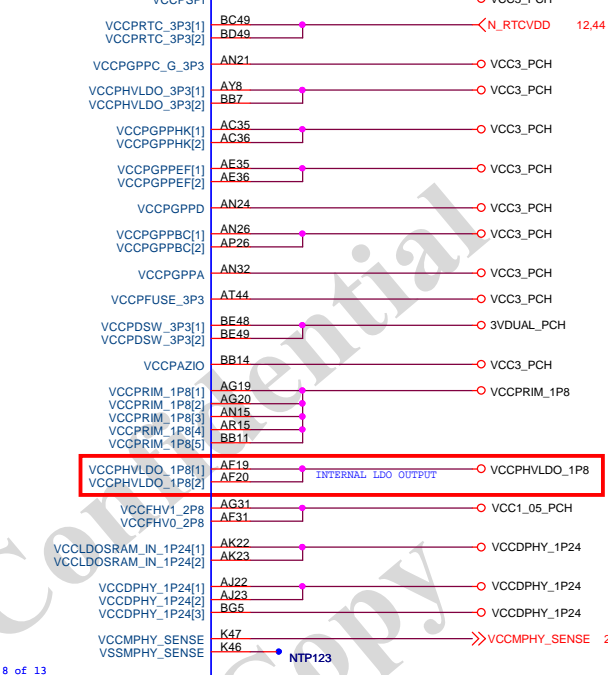
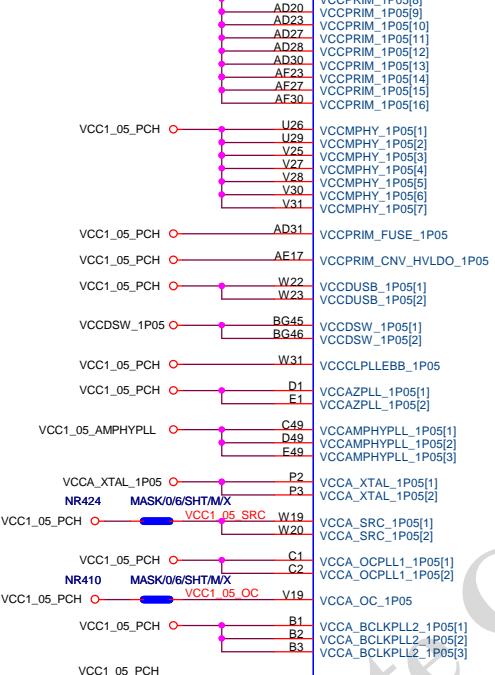
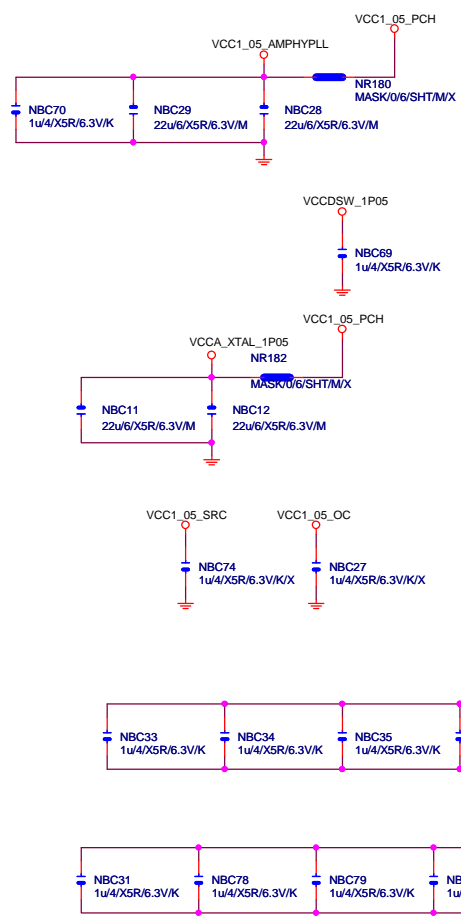


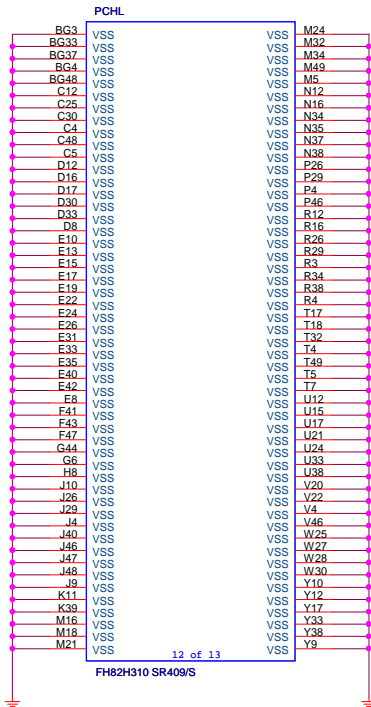
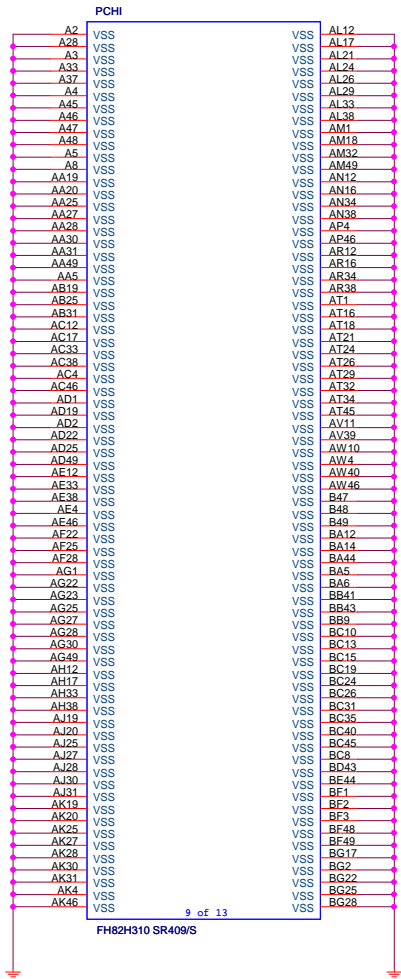
ITEM	PCIE P21	PCIE P22	PCIE P23	PCIE P24
H310	N/A	N/A	N/A	N/A
B360	PCIE	PCIE	PCIE	PCIE
Q360	PCIE	PCIE	PCIE	PCIE
H370	PCIE	PCIE	PCIE	PCIE
Z390	PCIE	PCIE	PCIE	PCIE
Q370	PCIE	PCIE	PCIE	PCIE

ANS 6187061			
<div style="text-align: center;">  </div>			
Title			
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CNP_R0.7





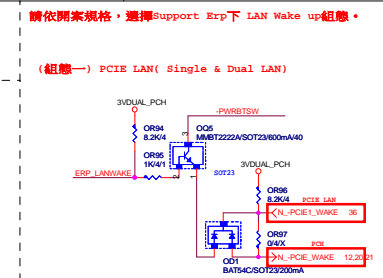
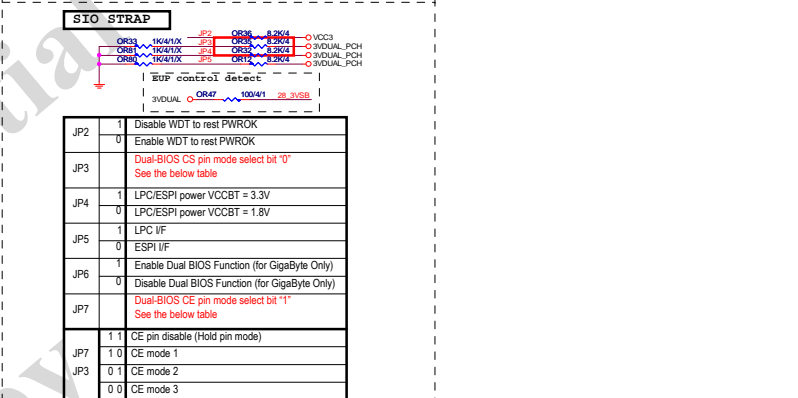
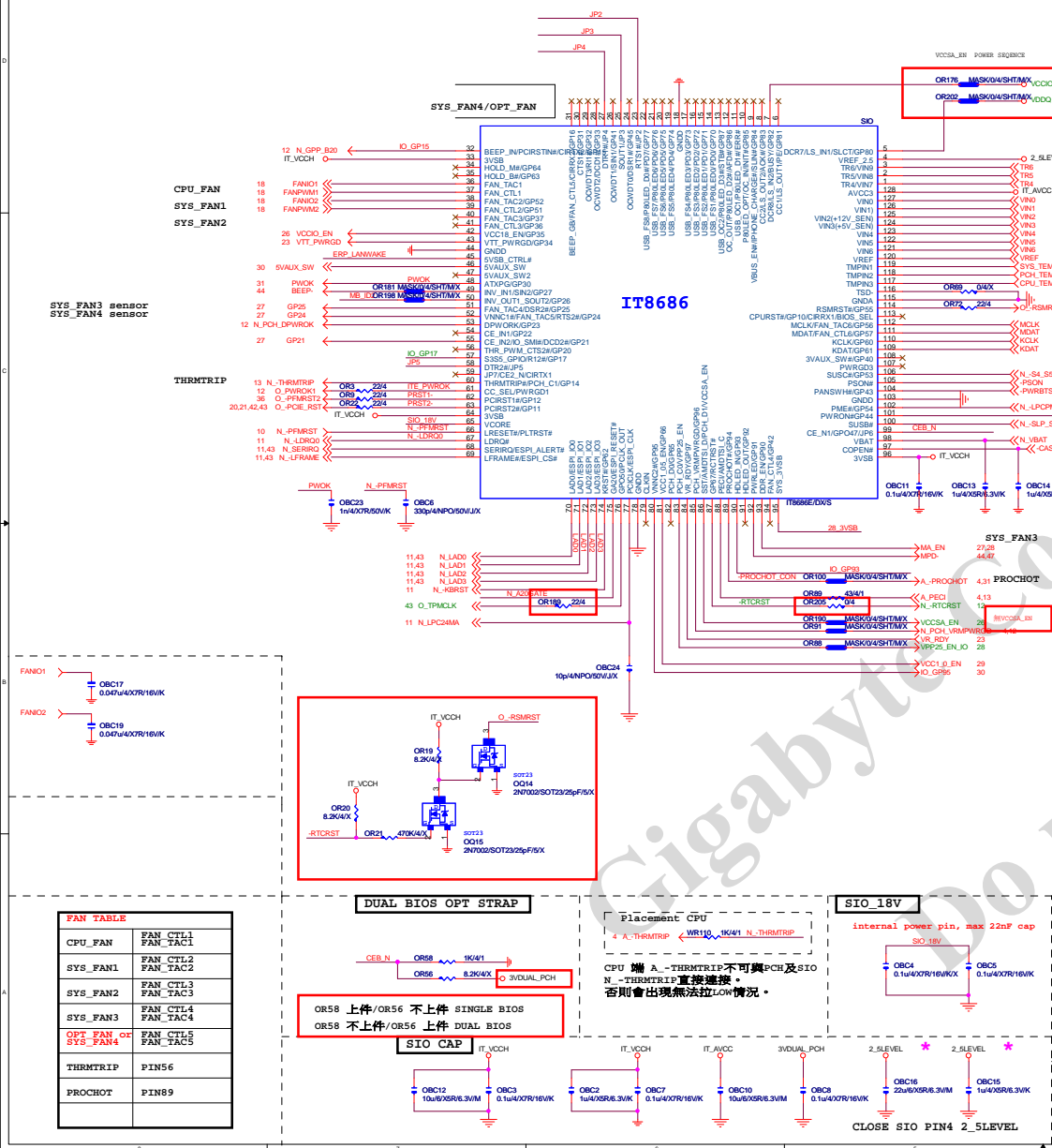


SB_HEATSIN

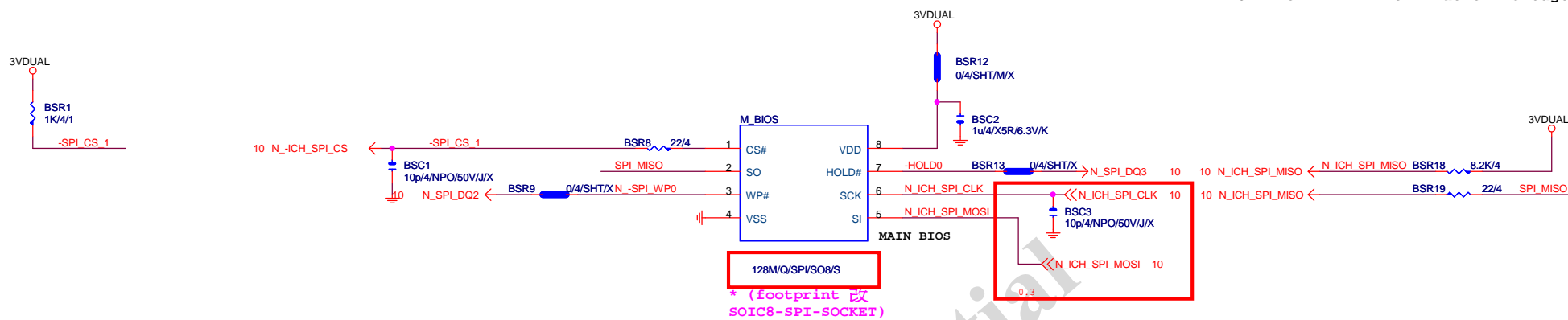
業務指定使用B150M-EVO Heatsink

BGAHSINK_SB-N

PCH_HS
PCH_HS[12SP2-030005-51R_12SP2-030005-52R_12SP2-030005-53R]

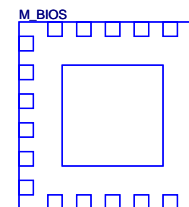


ERP Wake on LAN		
Single LAN	Realtek	組態一
	Atheros	
	Intel 219	組態二
	Dual LAN (只准一個 LAN 支持 ERP Wake Up)	Atheros+Atheros
No Support ERP	Intel 219+Atheros	組態三
	Intel 219+Intel 210	
		Single LAN BOM 只 OR97。 Dual LAN BOM 只 OR97、OR39。



BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating
0 means PD 1K



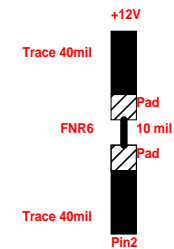
LCP/G-FL/1.27mm/200MIL/WHITE[10SL2-000008-31R]/X

* 試産先上, PVT 移除

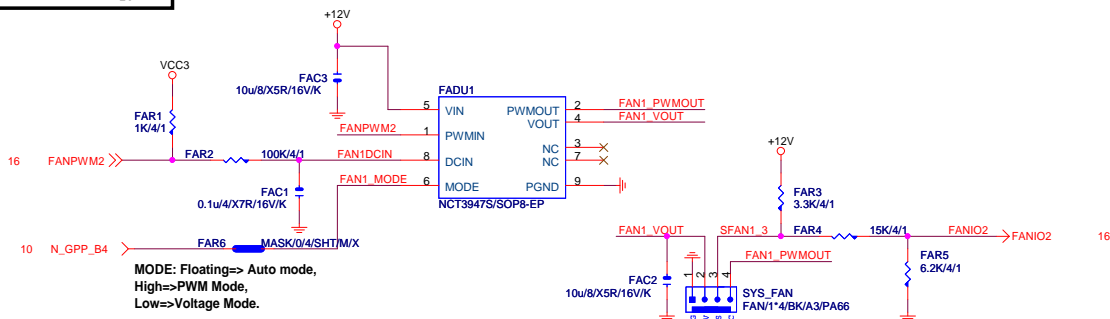
Gigabyte Technology

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BIOS			1.01
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Rev: 0.8

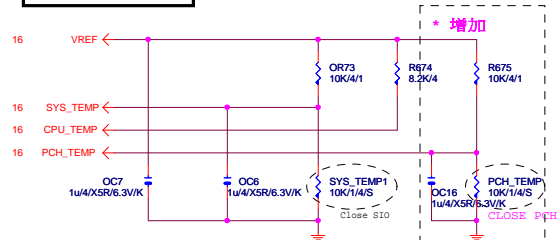


A. SYSTEM FAN1

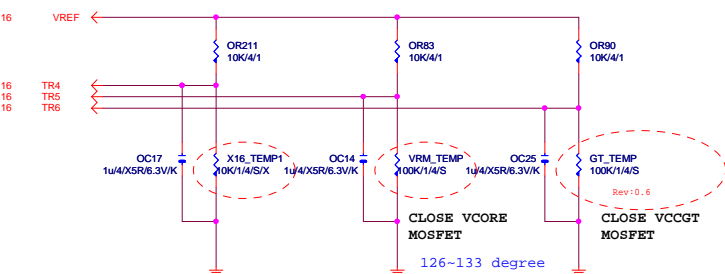


Title			
FAN CTRL			
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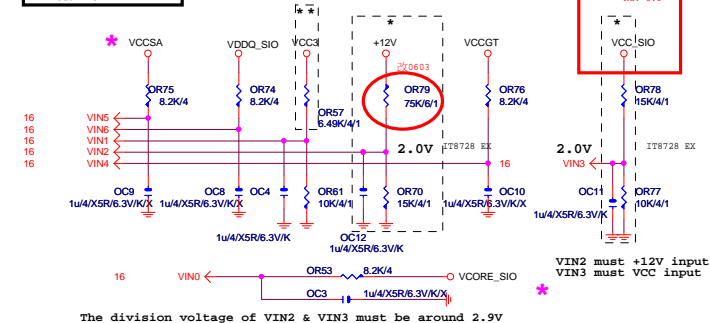
TEMP H/W MONITOR



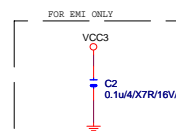
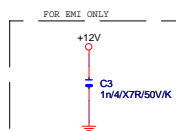
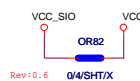
低階機種: 3個FAN時使用



VOLTAGE-- H/W MONITOR



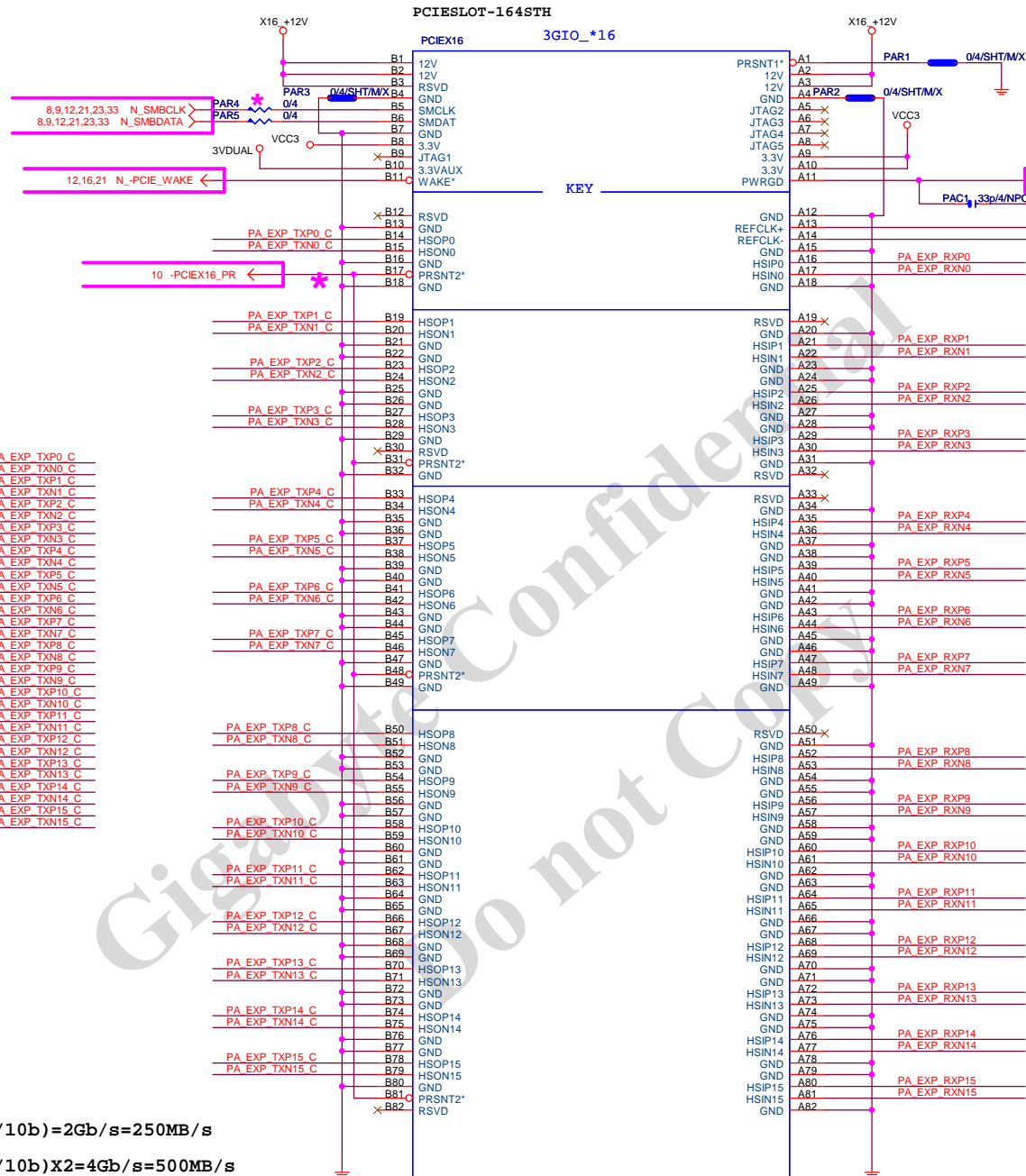
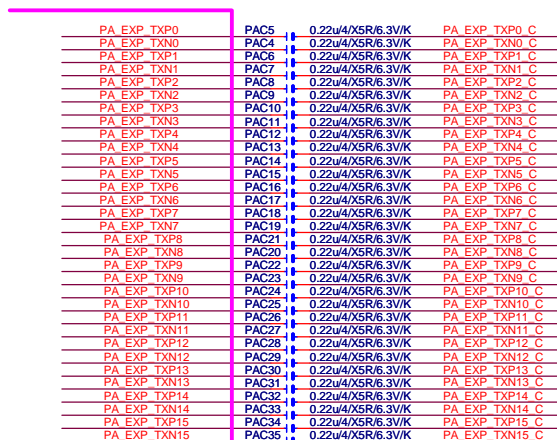
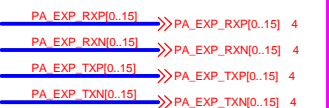
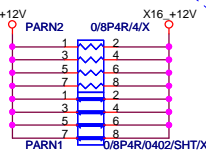
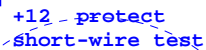
(靠近ATX CONNECTOR)



★Update 2015-04-24

Gigabyte Technology

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PCIEX16:16/5/5/5/16

PCI-E REV:1.1--> 2.5GHZ

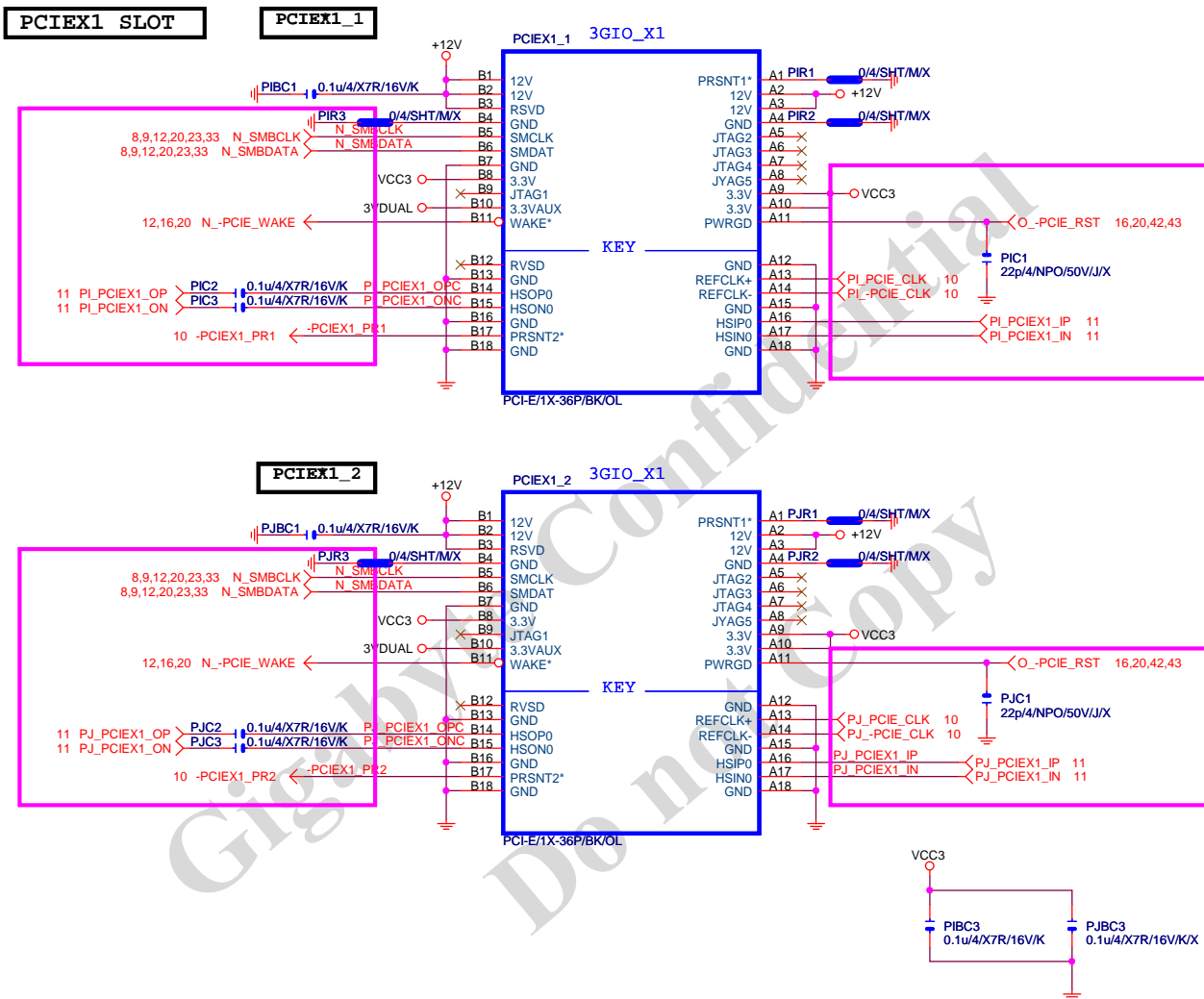
PCE-E X1(單向) BANDWIDTH=2.5GHz*(8b/10b)=2Gb/s=250MB/s

PCE-E X1(雙向) BANDWIDTH=2.5GHz*(8b/10b)X2=4Gb/s=500MB/s

PCE-E X16(單向) BANDWIDTH=2.5GHz*(8b/10b)X16=32Gb/s=4GB/s

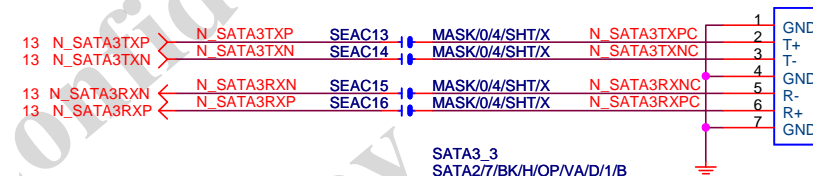
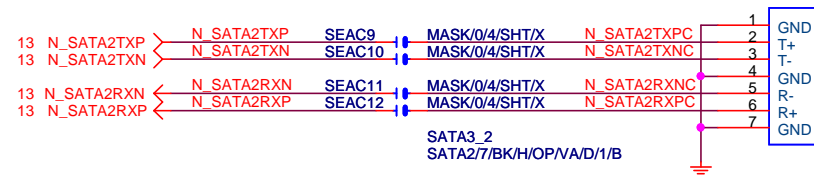
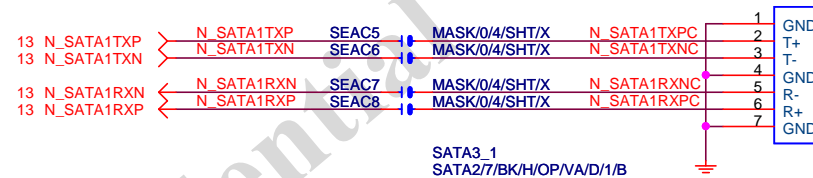
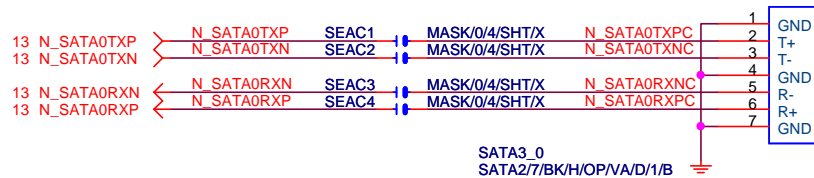
PCE-E X16(雙向) BANDWIDTH=2.5GHz*(8b/10b)X16X2=64Gb/s=8GB/s

PCI-E REV:2.0--> 5GHZ

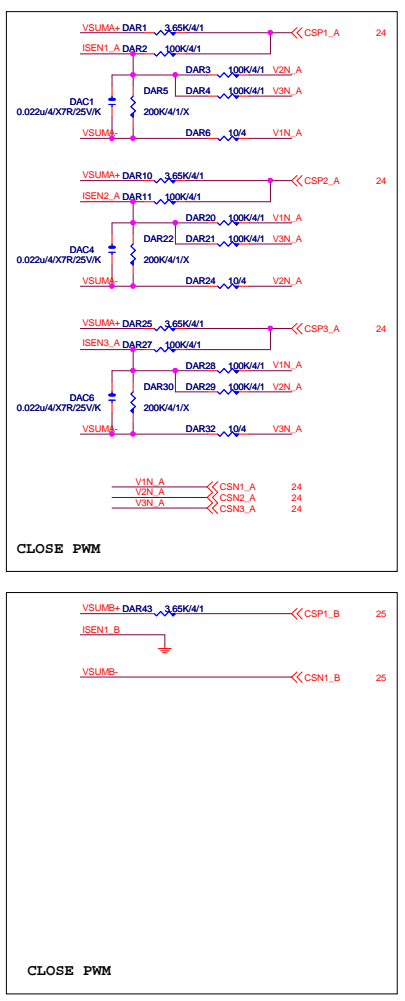


Gigabyte Technology

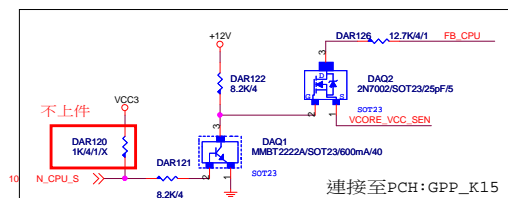
Title			PCIE_X1 1,2
Size	Document Number	H310M H	
Custom		Rev 1.01	
Date:	Tuesday, February 27, 2018	Sheet 21	of 50

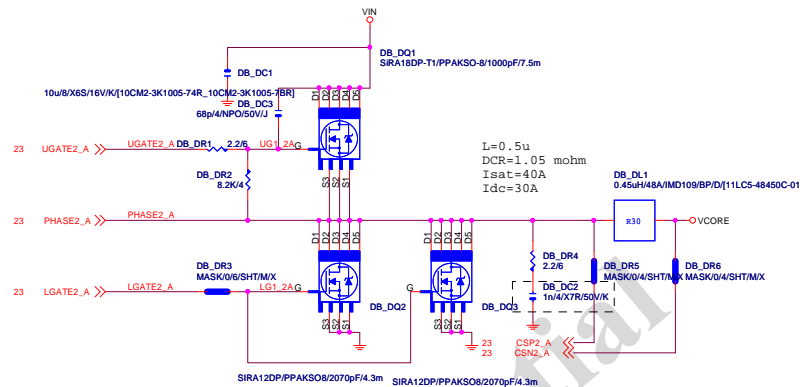
**Gigabyte Technology**Title
SATA EXPRESSSize Custom
Document Number
H310M HRev
1.01

Date: Tuesday, February 27, 2018 Sheet 22 of 50

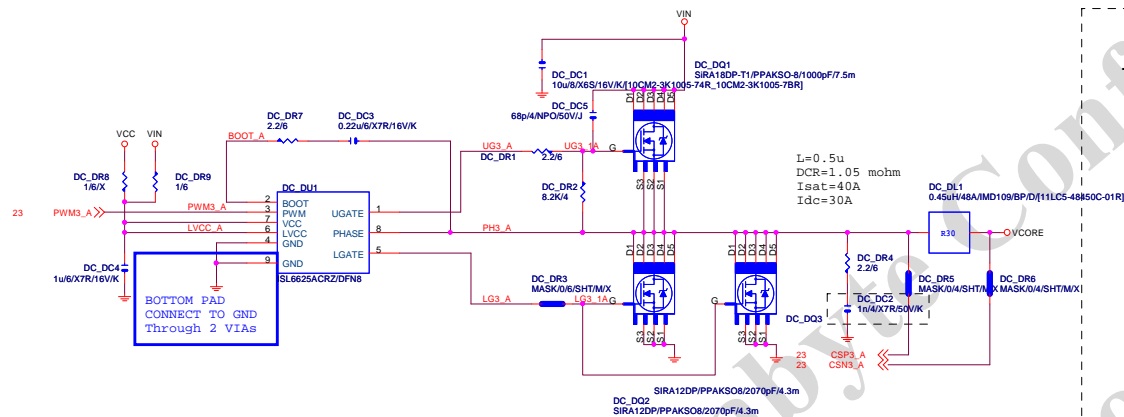
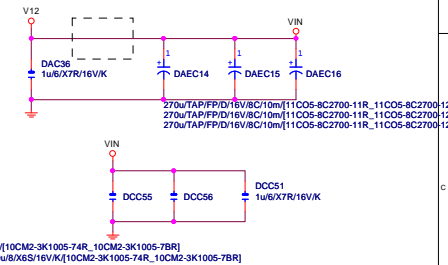


VSCORE	ISL95858	ISL95868	VCCGT	ISL95858	ISL95868
DAR137	X	V	DAR140	X	V
DAR138	V	X	DAR141	V	X
DAR139	X	V	DAR142	X	V
DAC15	V	X	DAC27	V	X
DAR79	V	X	DAR80	V	X
DAR33	V	X	DAR51	V	X

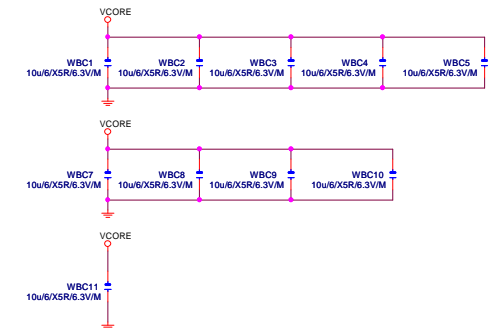
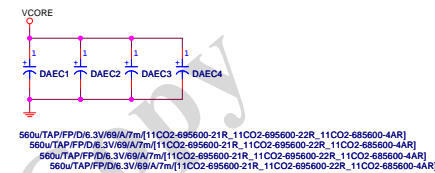




VIN CAP 270u*3PCS

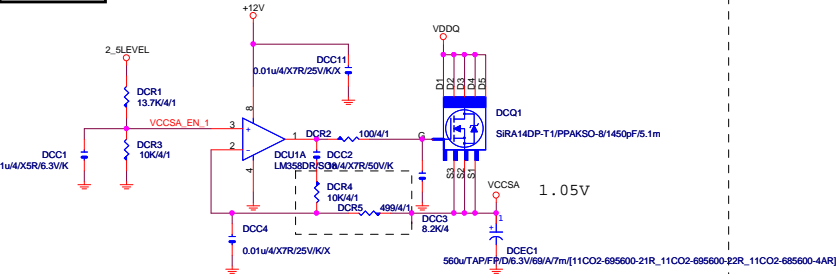


VCORE CAP 560u*4PCS
22u*10PCS

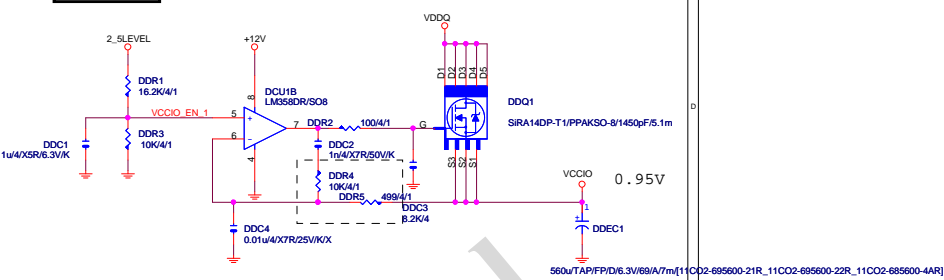


1H1L規格時移除

VCCSA



VCCIO



Connect to IT8686

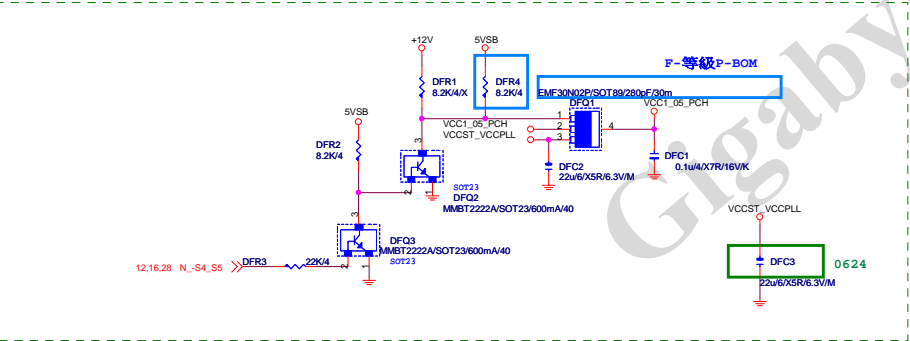
Connect to IT8686

Connect to PCH

SIO PIN5 . PIN7 用在其他function時
DCQ2 不上件
DDR7 不需要預留

SIO PIN5 . PIN7接VDDQ . VCCIO時
DCQ2 不上件
DDR7 不上件

VCCST_VCCPLL



DDR4



16 GP25 MA_DR46 9.1K/4/1

16 GP24 MA_DR21 26.1K/4/1

16 GP21 MA_DR22 6.8K/4/1



L=1u
DCR=2.5 mohm
Isat=35A
Idc=28A

Remote sense請從最重的負載端點拉回

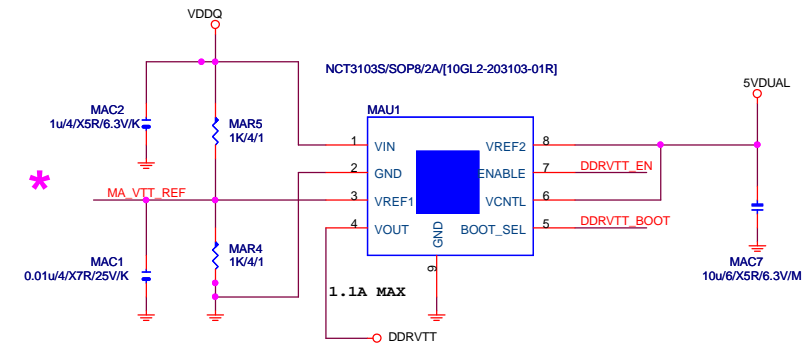
PWR SEQ

CLOSE TO DDR POWER PLANE

For power sequence require

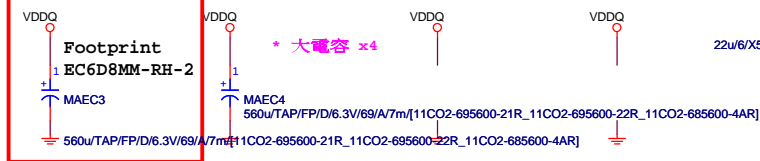
VPP_25V使用8120時上件

MAU1上RT9045時上件(不可MASK)

DDRVTT

DDR CAP


560u*4PCS 22u*2PCS



* 大電容 x4

DDRVTT CAP

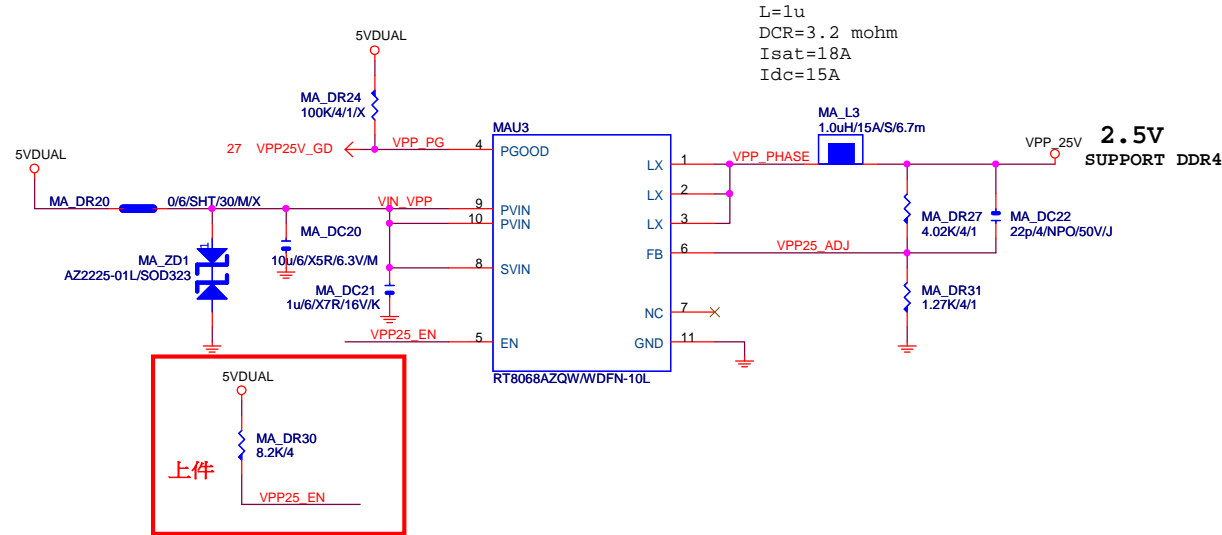
* 大電容 x

			
Title			
RT8237_DDR4 POWER			
Size	Document Number		Rev
Custom	H310M H		1.01
Date:	Tuesday, February 27, 2018	Sheet 27 of	50

REV:0.4

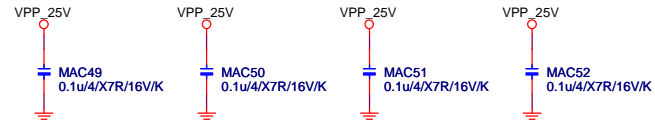
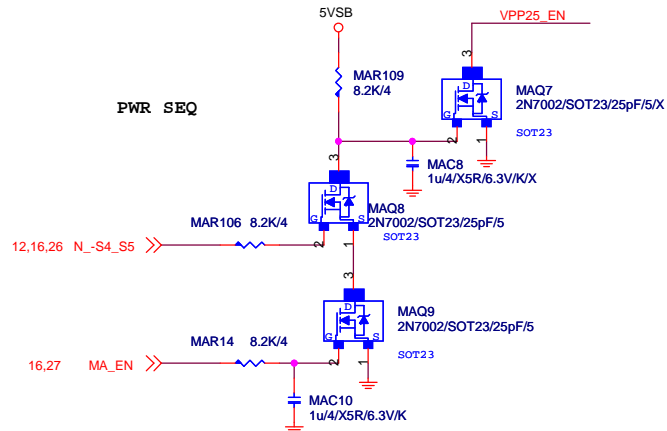
VPP_25V

CHOKE與CAP料號可變



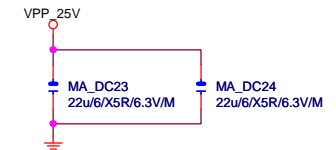
PWR_SEQ

* 刪 MA_DR32



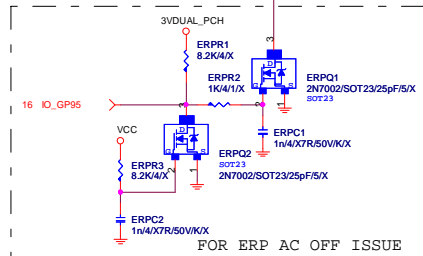
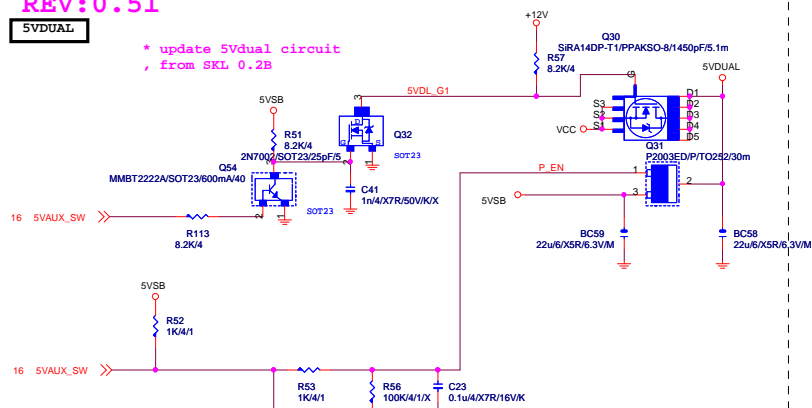
VPP CAP 22u*1PCS

* 大電容 x0

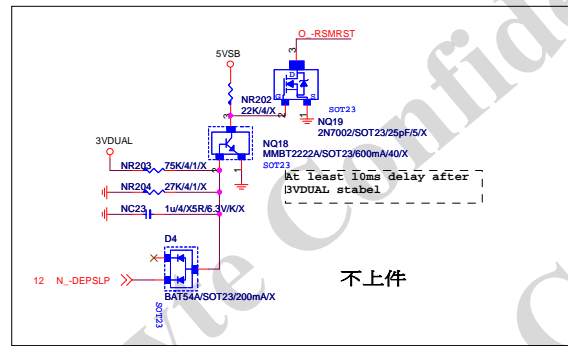
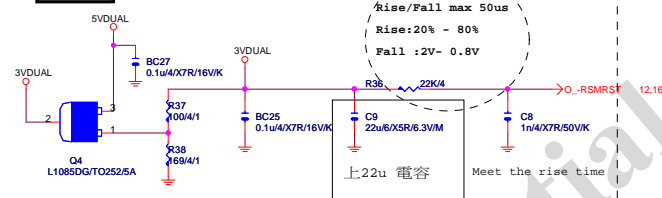


GIGABYTE™			
Title RT8068A_VPP25 POWER			
Size Custom	Document Number H310M H	Rev 1.01	
Date: Tuesday, February 27, 2018	Sheet 28	of 50	

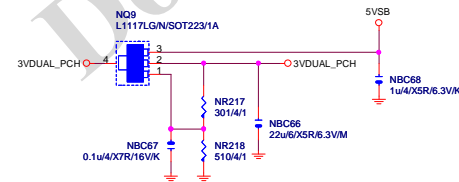
* update 5Vdual circuit
from SKL 0.2B



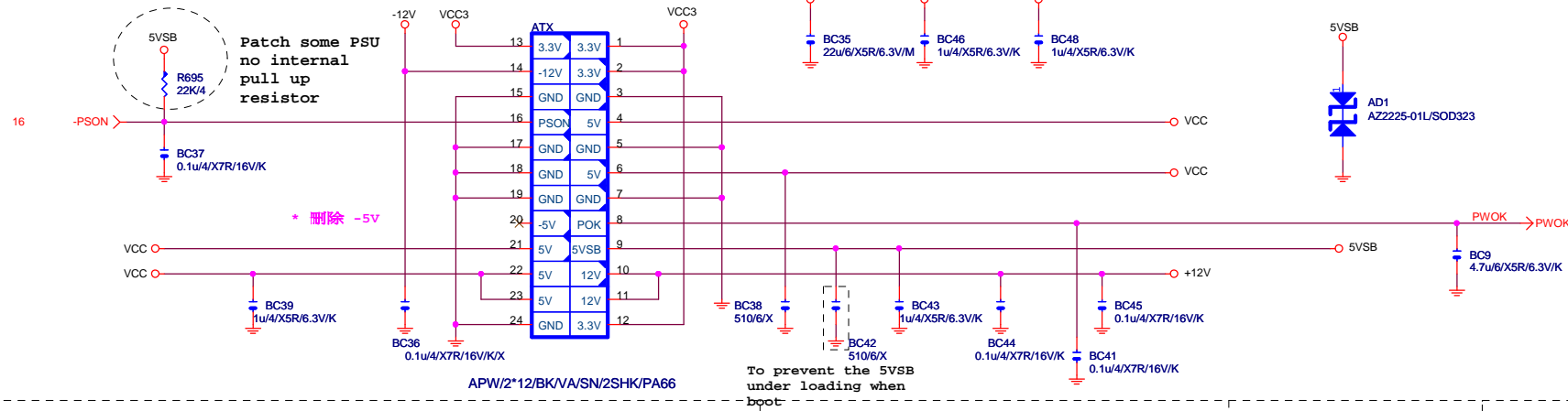
3VDUAL



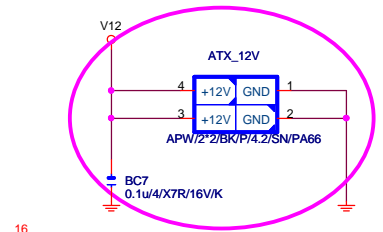
3VDUAL_PCH



ATXX24 POWER CONNECTOR

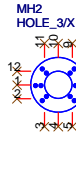
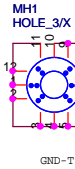


ATXX4 POWER CONNECTOR

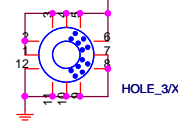


螺絲孔

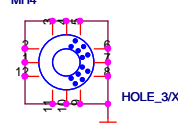
MH1:GND-T
FOR EMI
TEST驗證



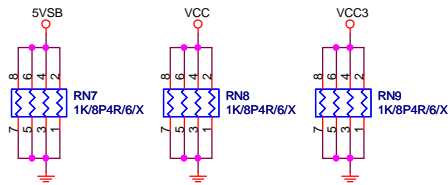
14/12/24
Modify for EMI
MH3



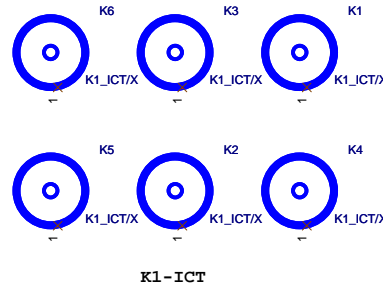
HOLE_4-RH-5MM-2



DUMMY LOAD



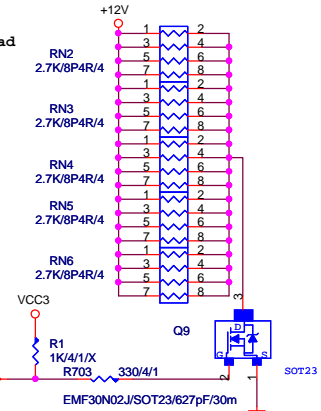
固定孔/光學點



To prevent the 5VSB under loading when boot

+12V DUMMY LOAD

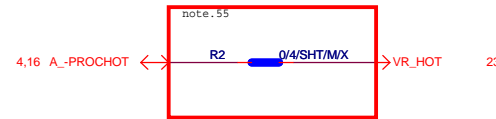
To fix 12V light load abnormal issue



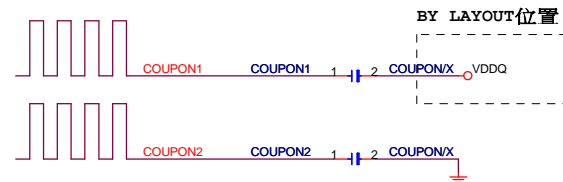
【技術通報R&D技術通報153】

-PROHOT

* 保留 ?



COUPON



BY LAYOUT位置

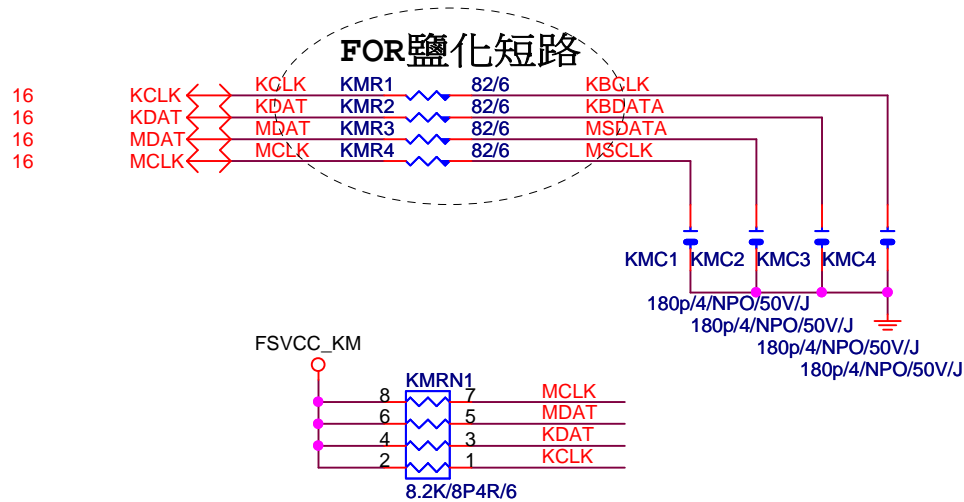
Gigabyte Technology

Title	ATX POWER CONNECTOR		
Size	Document Number	H310M H	
Custom			Rev 1.01
Date:	Tuesday, February 27, 2018	Sheet 31	of 50

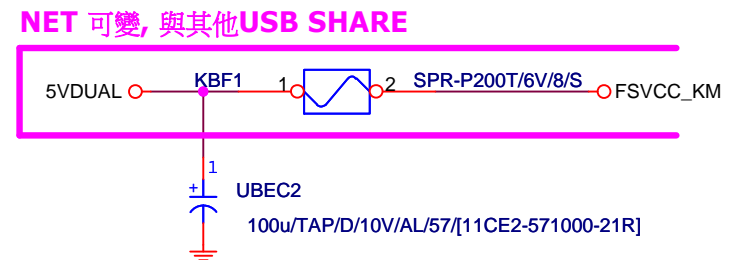
Rev: 0.7



KB_MS_USB DAMPING/PU



KB_MS_USB PWR



USB OC PROTECT

Gigabyte Technology

Title	Author	Year	Journal	Volume	Issue	Page
1. The Effect of Temperature on the Rate of Reaction	John Doe	2018	Journal of Chemical Education	95	3	456-462
2. Kinetics of the Reaction Between Hydrogen Peroxide and Potassium Iodide	Jane Smith	2017	Journal of Chemical Education	94	2	321-328
3. The Effect of Concentration on the Rate of Reaction	Michael Brown	2016	Journal of Chemical Education	93	1	123-130
4. The Effect of Surface Area on the Rate of Reaction	Sarah White	2015	Journal of Chemical Education	92	4	567-574
5. The Effect of Catalyst on the Rate of Reaction	David Green	2014	Journal of Chemical Education	91	5	678-685

KB_MS_USBSize
A

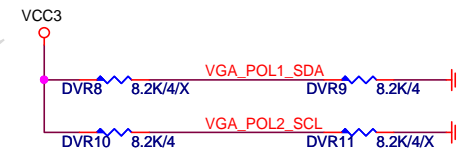
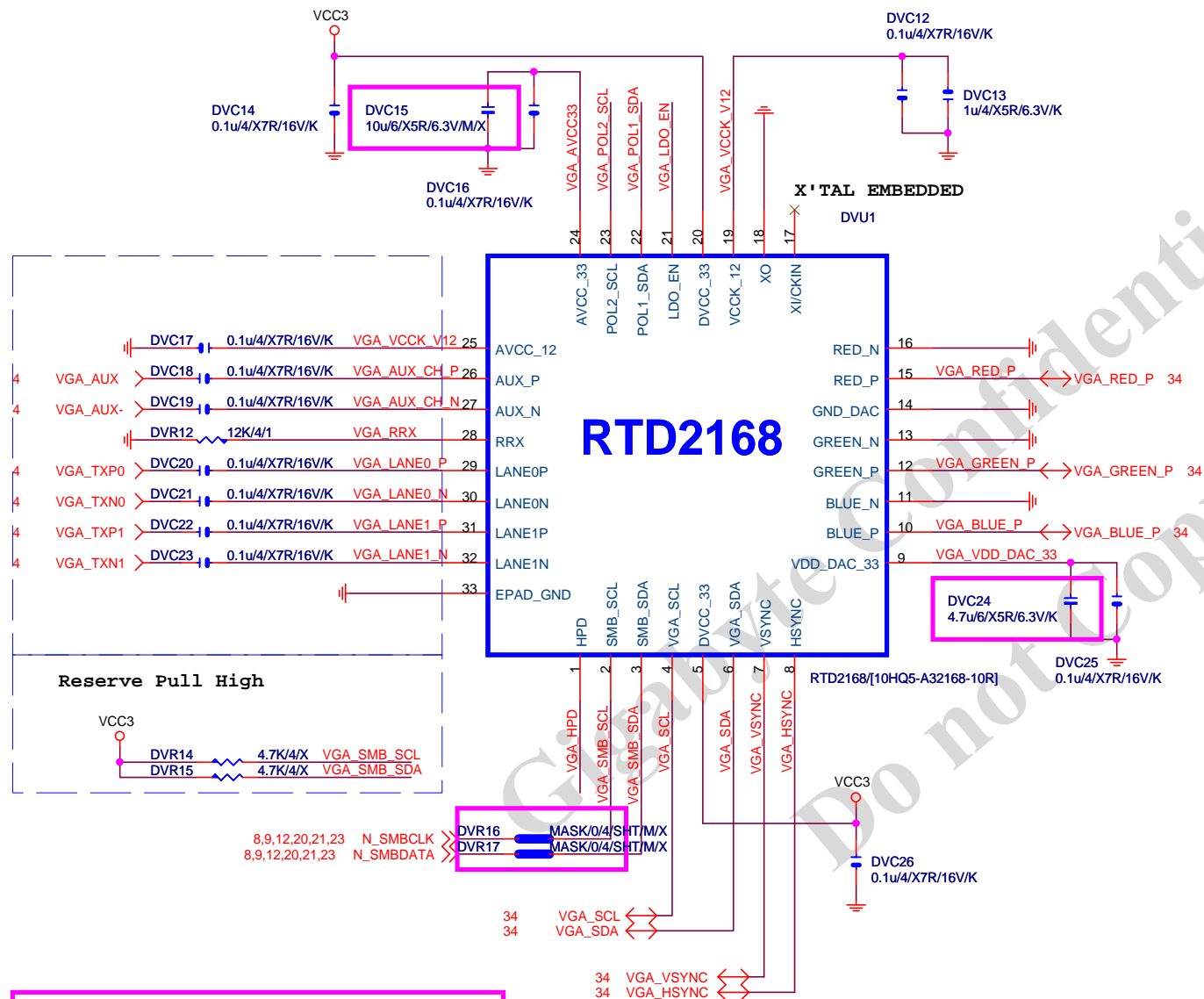
Document Number

Rev
1.01

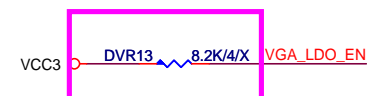
H310M H

Date: Tuesday, February 27, 2018

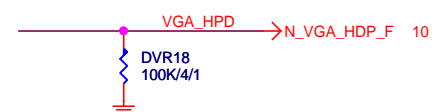
Sheet 32 of 50



		POL1_SDA(PIN22)	
		0	1
POL2_SCL (PIN23)	0	X	EP MODE
	1	ROM ONLY MODE	EEPROM MODE

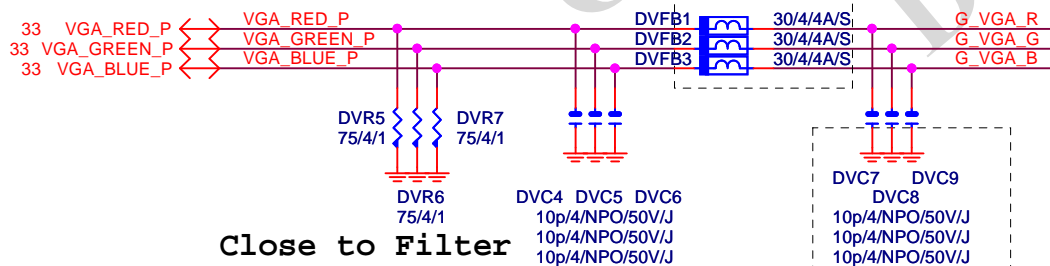
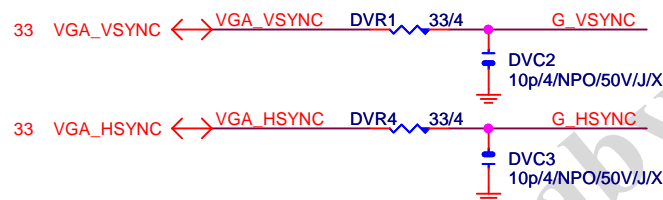
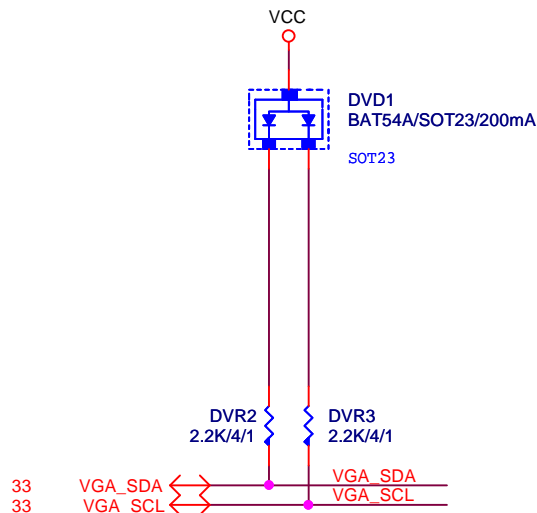


LDO_EN(PIN21)	
0	1
VCC33 from External 1.2V	VCC33 from Embedded LDO



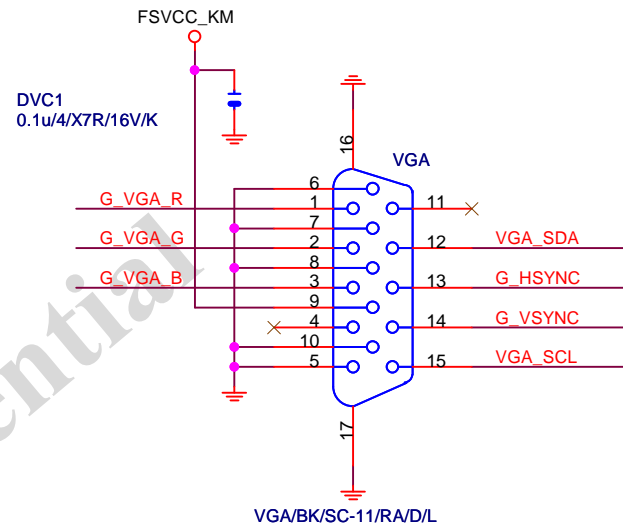
Gigabyte Technology
DP-VGA RTD2168

VGA SIGNAL R2.0

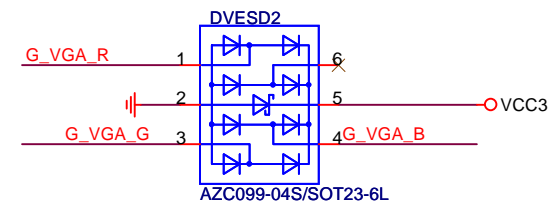
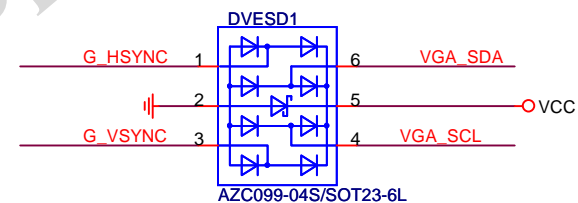


FOR EMI

VGA CONN.

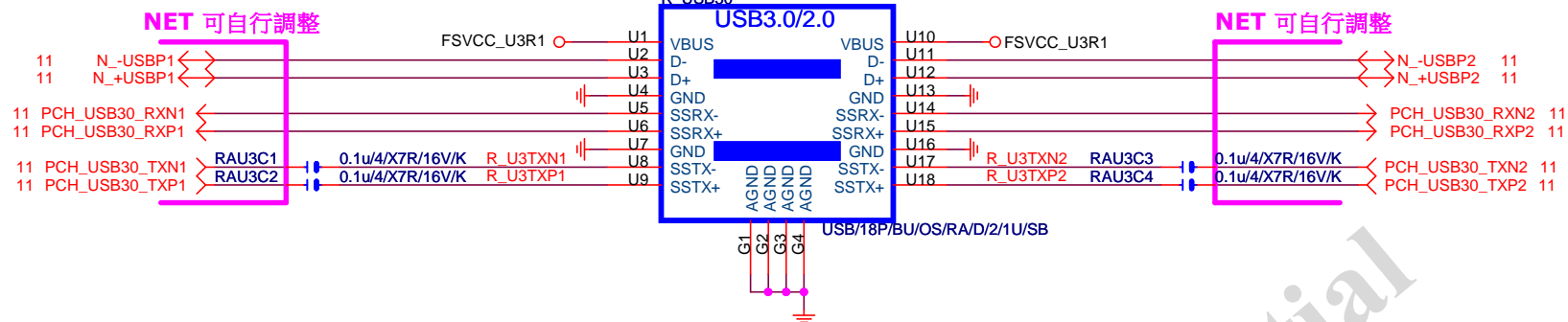


VGA ESD



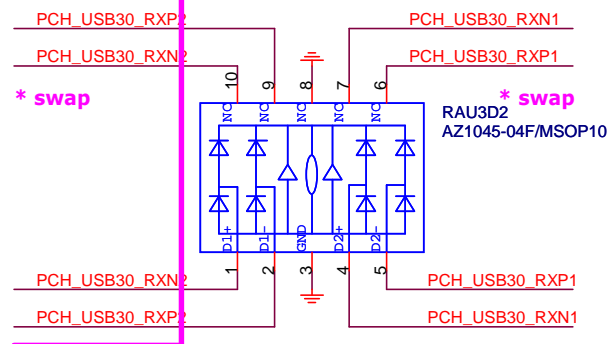
Gigabyte Technology
NXP-PTN3356

Title		
Size	Document Number	Rev
Custom	H310M H	1.01
Date:	Tuesday, February 27, 2018	Sheet 34 of 50

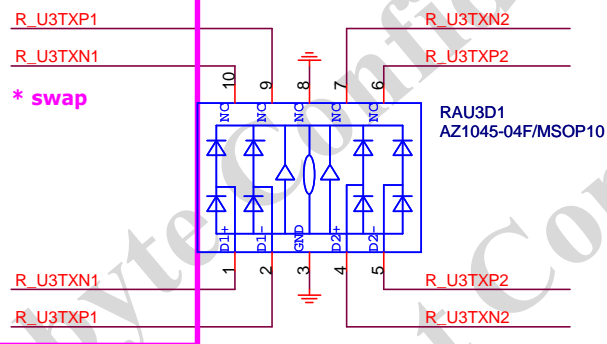


ESD

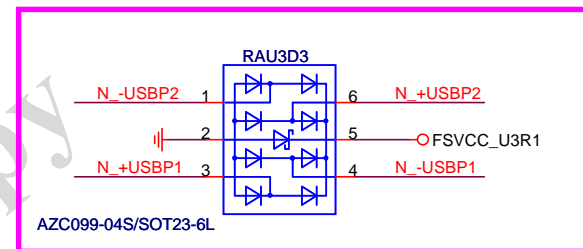
NET 可自行調整



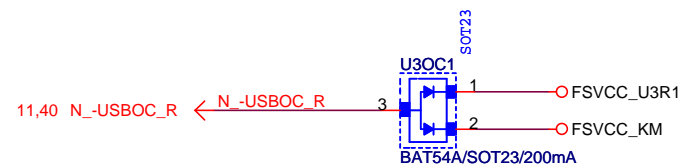
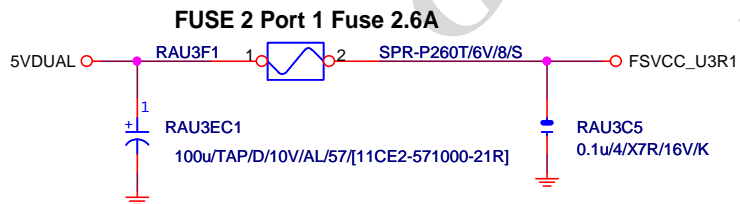
NET 可自行調整



NET 可自行調整



FUSE



Gigabyte Technology

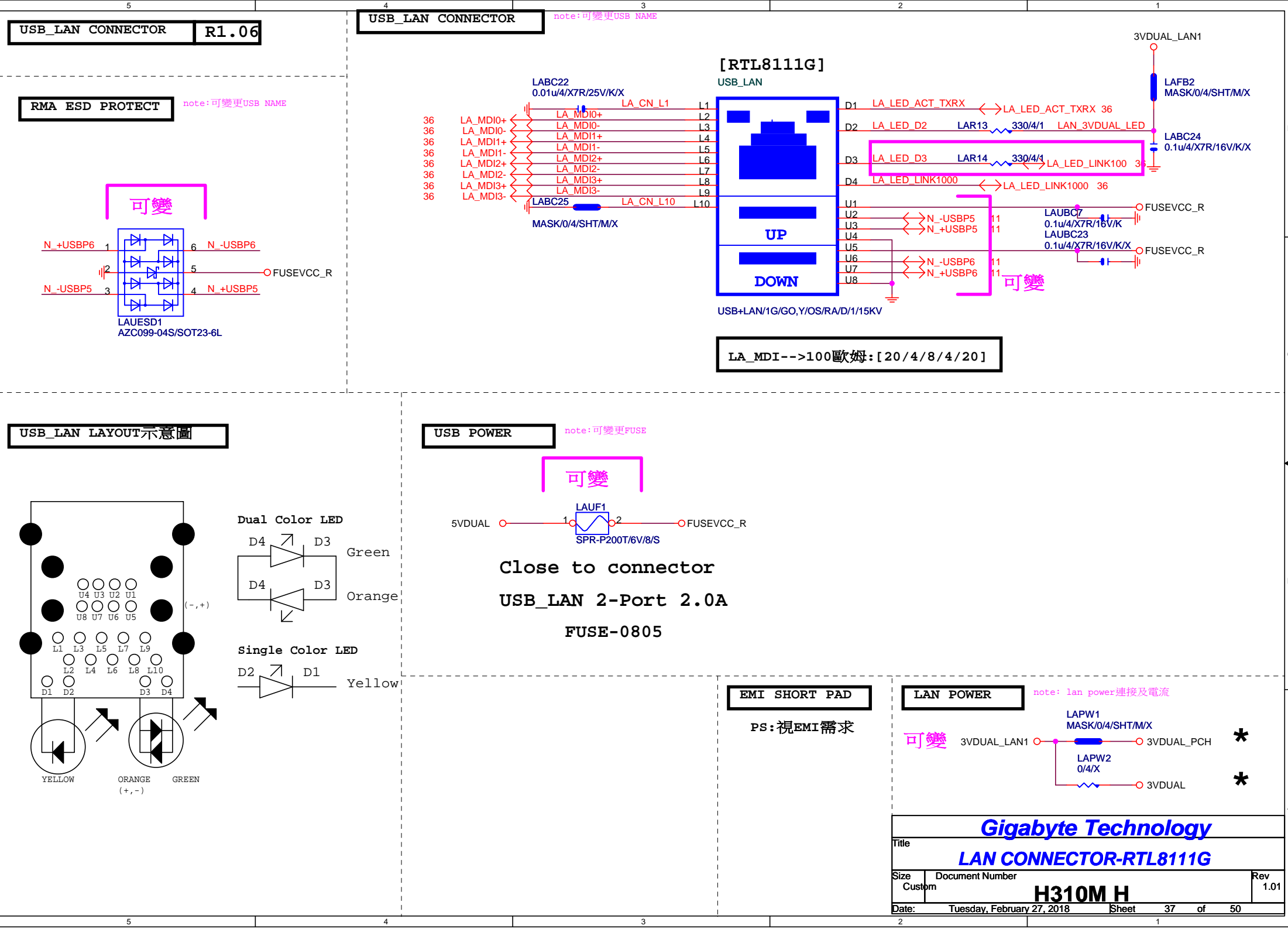
Title		
R_USB30,USB_OC		
Size	Document Number	Rev
Custom	H310M H	1.01
Date:	Tuesday, February 27, 2018	Sheet 35 of 50

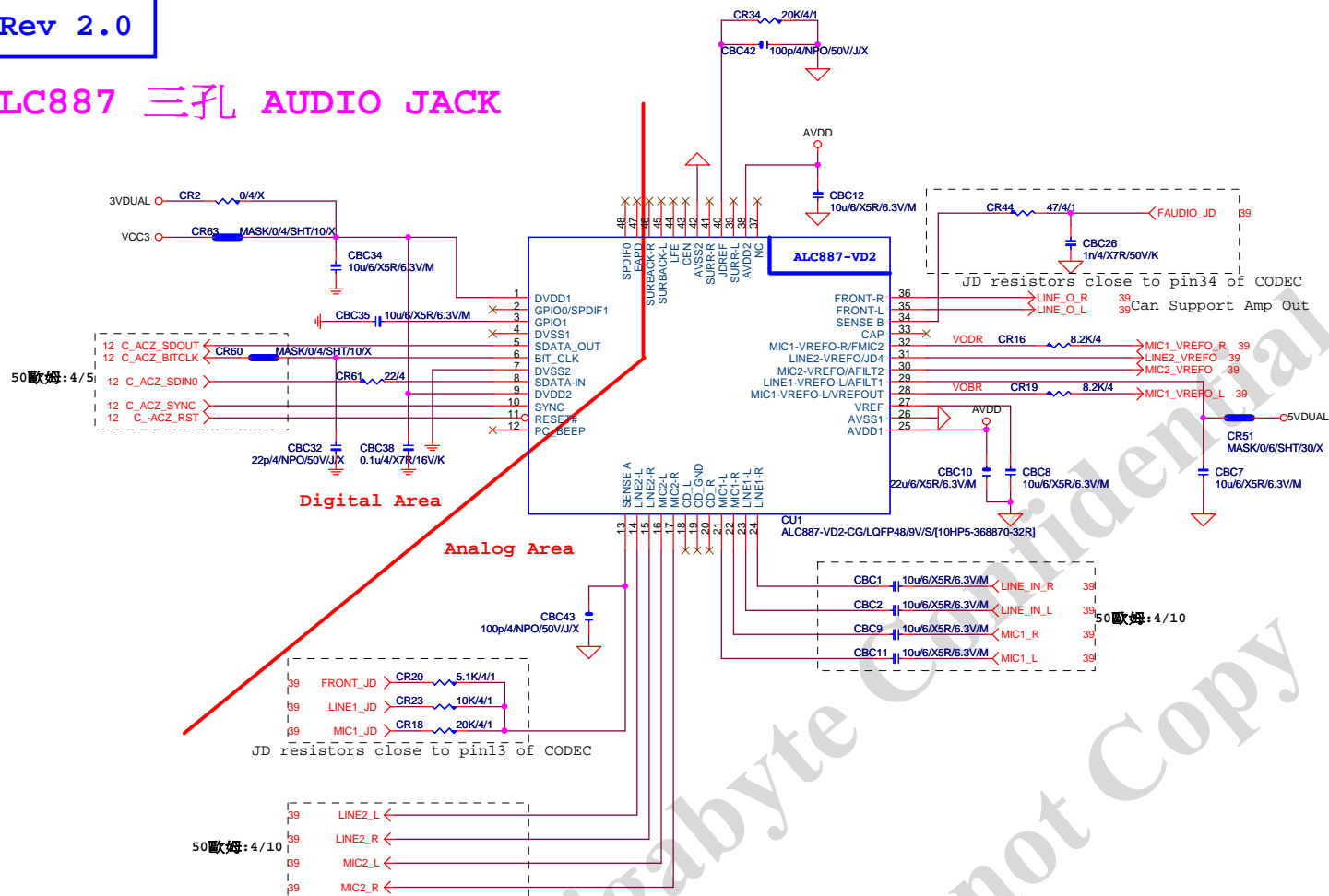


LA_ ML-->80歐姆:[15/5/5/5/15]

MDI ESD預留

*





LAYOUT注意:螺絲孔下GND方式

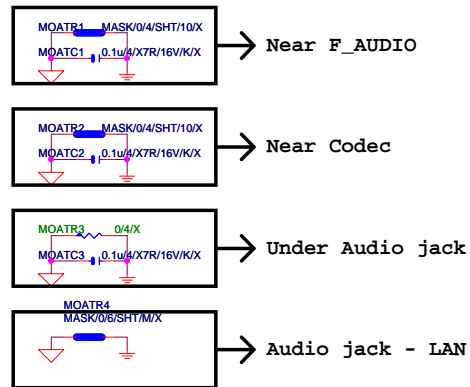
1. MH1空間夠,下DGND
空間不夠,改為Isolate
2. MH2一律改為Isolate

<input type="radio"/> MH1 DGND	<input type="radio"/> MH2 Isolate	
-----------------------------------	--------------------------------------	--

LAYOUT注意:要加
GND切割線

音效區域印刷

Rev 2.01

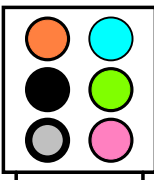


*量産前, 0ohm改short pad

SPDIF_OUT

SPDIF_IN

AZALIA JACK

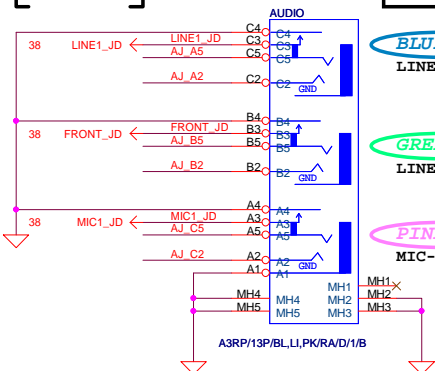


AZALIA JACK

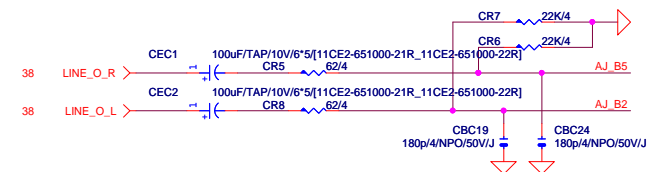
BLUE
LINE-IN

GREEN
LINE-OUT

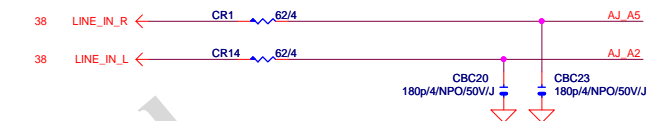
PINK
MIC-IN



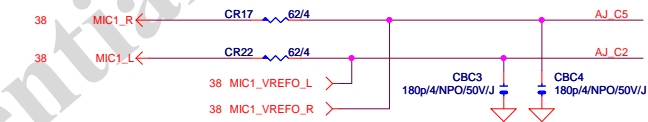
LINE-OUT



LINE-IN



MIC-IN

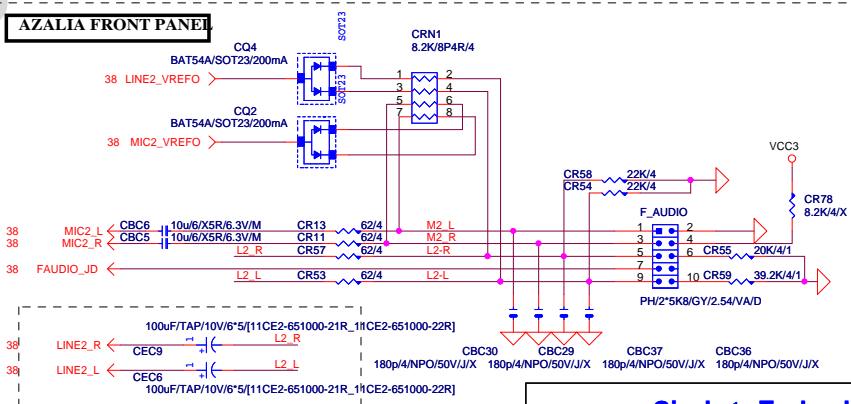


SURROUND

CEN/LFE

SURR BACK

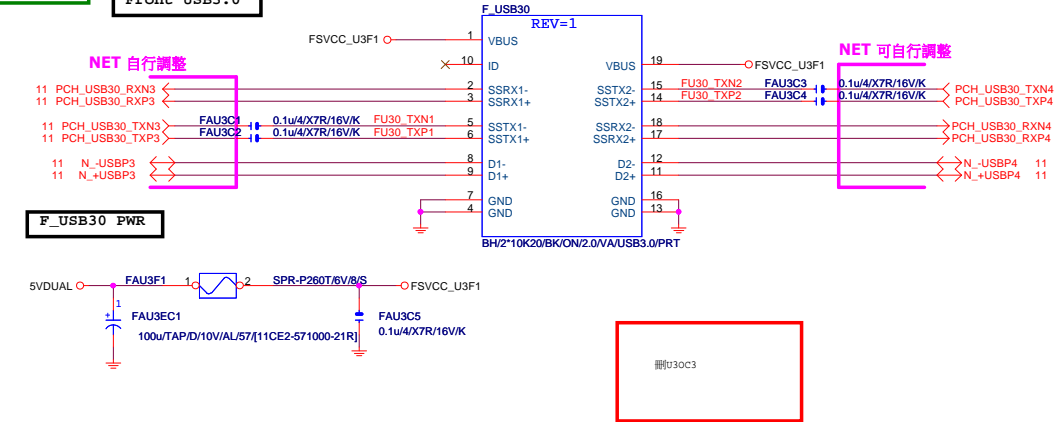
AZALIA FRONT PANEL



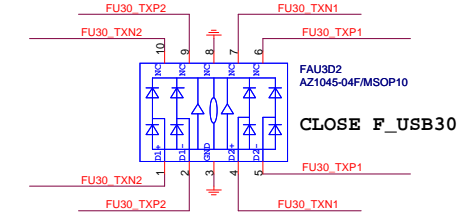
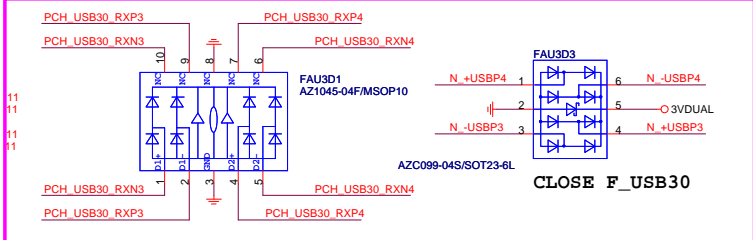
Gigabyte Technology

Title			AUDIO JACK	
Size			H310M H	
Date:			Tuesday, February 27, 2018	
Sheet			39 of 50	
Rev			1.01	

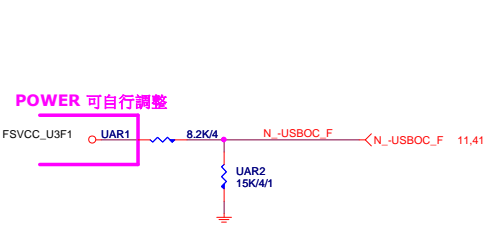
Front USB3.0



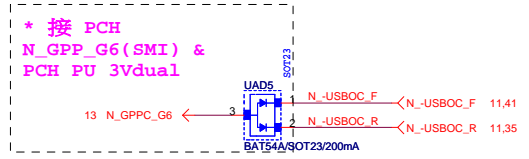
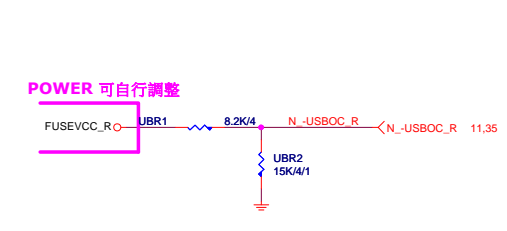
NET 可自行調整

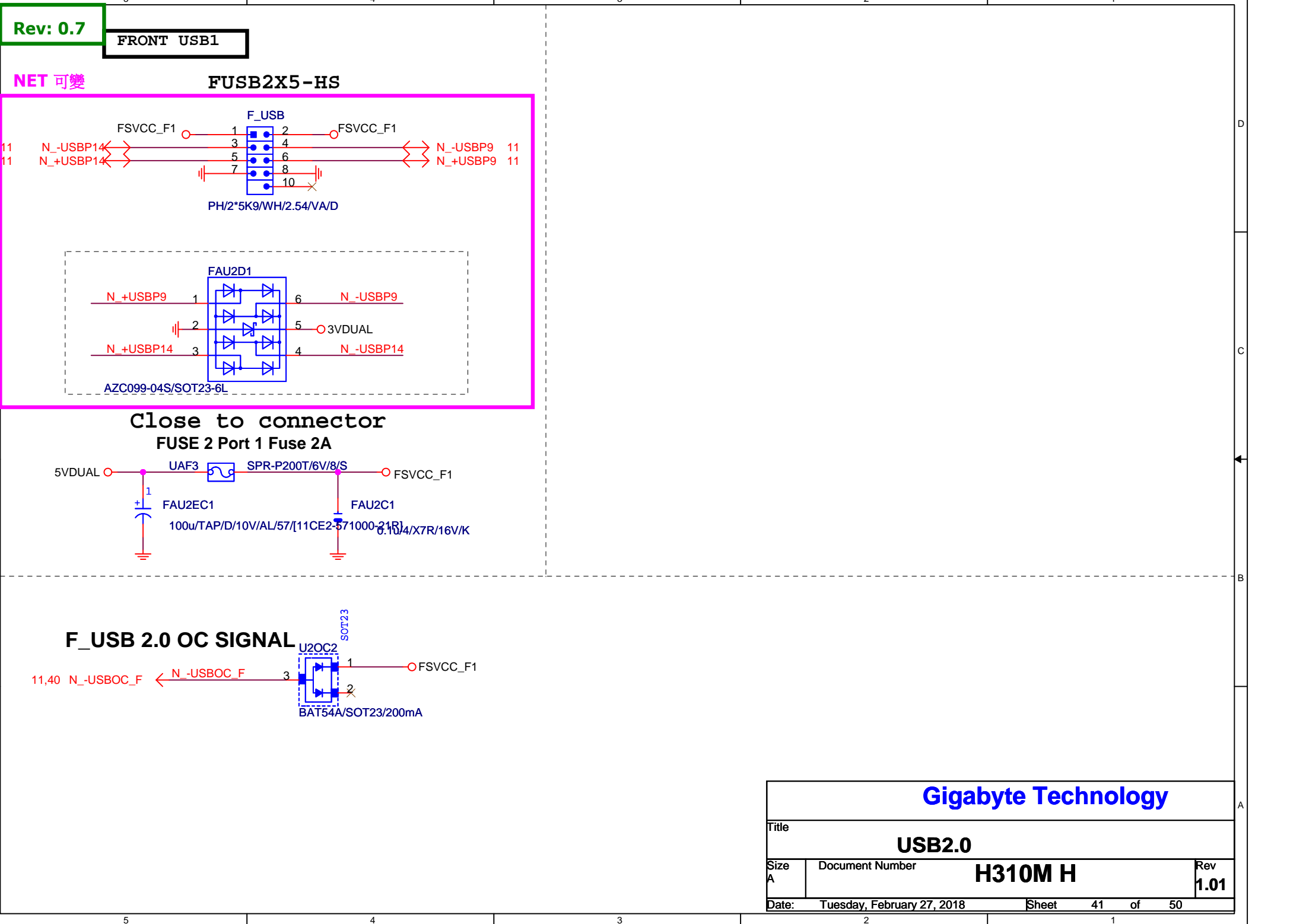


-USBOC_F



-USBOC_R





Gigabyte Technology

Title

USB2.0

Size A

Document Number

H310M H

Rev

1.01

Date: Tuesday, February 27, 2018

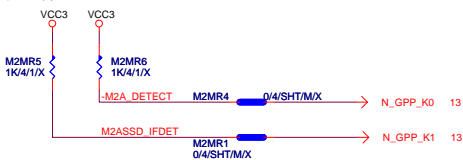
Sheet 41 of 50

Rev 0.4

M.2 Lane2 from PCH port11

M.2 Lane2 from PCH port12

支援SATA and M.2 function



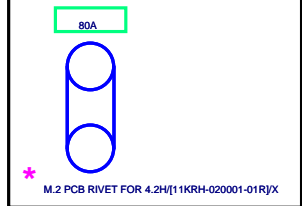
需與M2_-CLKREQ對應

SATA : GND.
PCIe : HIGH

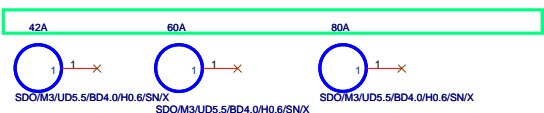
M2插卡時為Low

* Footprint : NGFF-M-75P-8CM-09MM-SMD
MASK:NGFF-M-8CM-09MM-SMD-MASK

M.2扣具



SMD螺柱



* Footprint : HOLE_165NP
MASK:HOLE_165NP-MASK

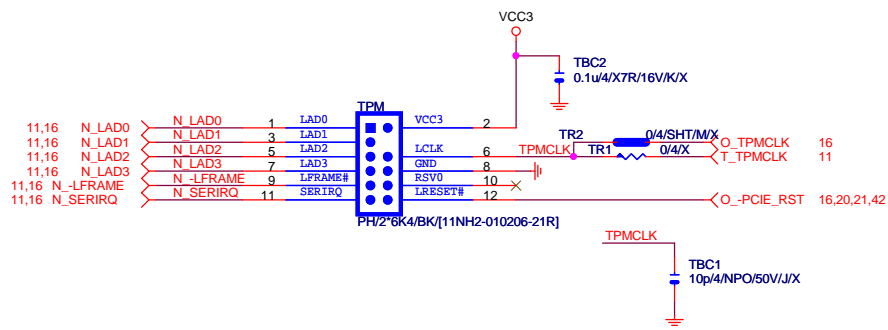
Gigabyte Technology

Title			M.2 X4
Size	Custom	Document Number	H310M H
Date:	Tuesday, February 27, 2018	Sheet	42 of 50

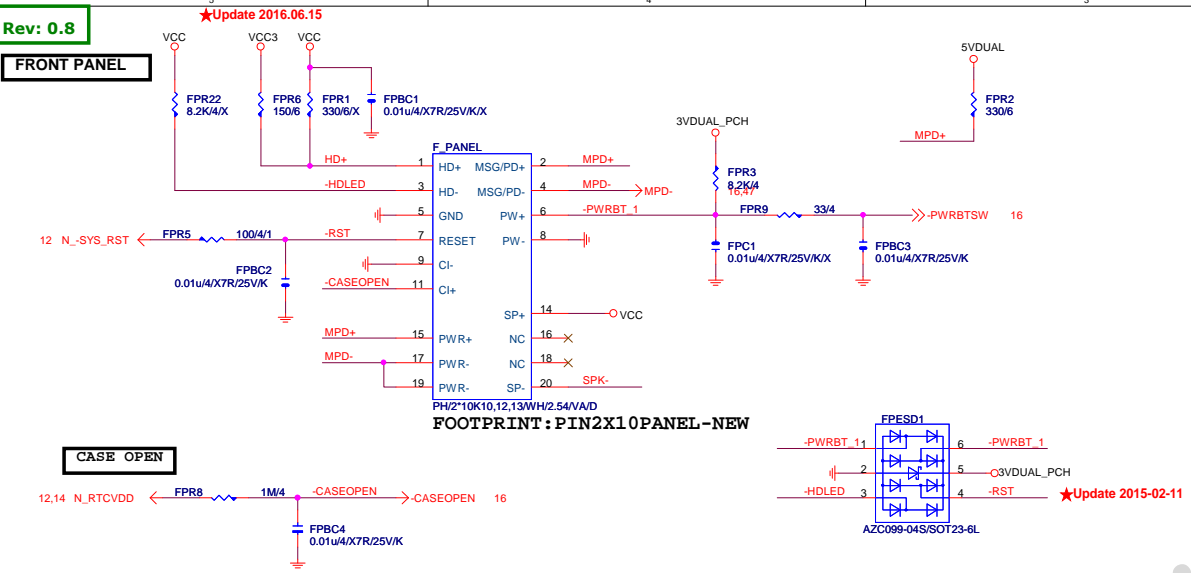
COM PORT

TPM CONNECT

Thunderbolt (N/A)



FRONT PANEL

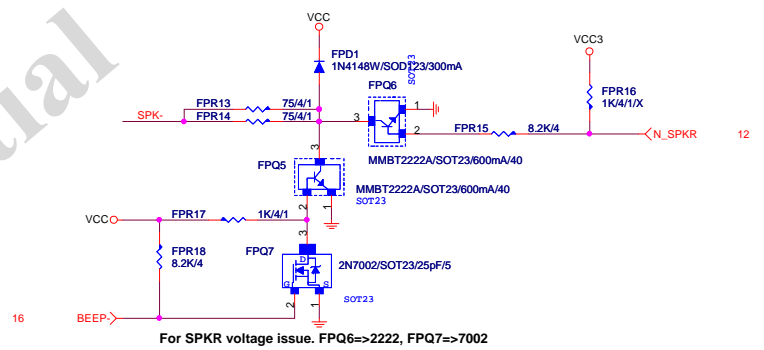
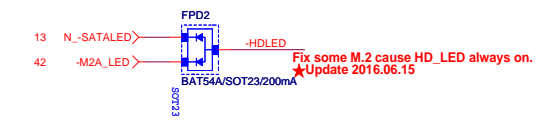


CASE OPEN

FRONT PANEL SHORT

SATA/M.2 LED

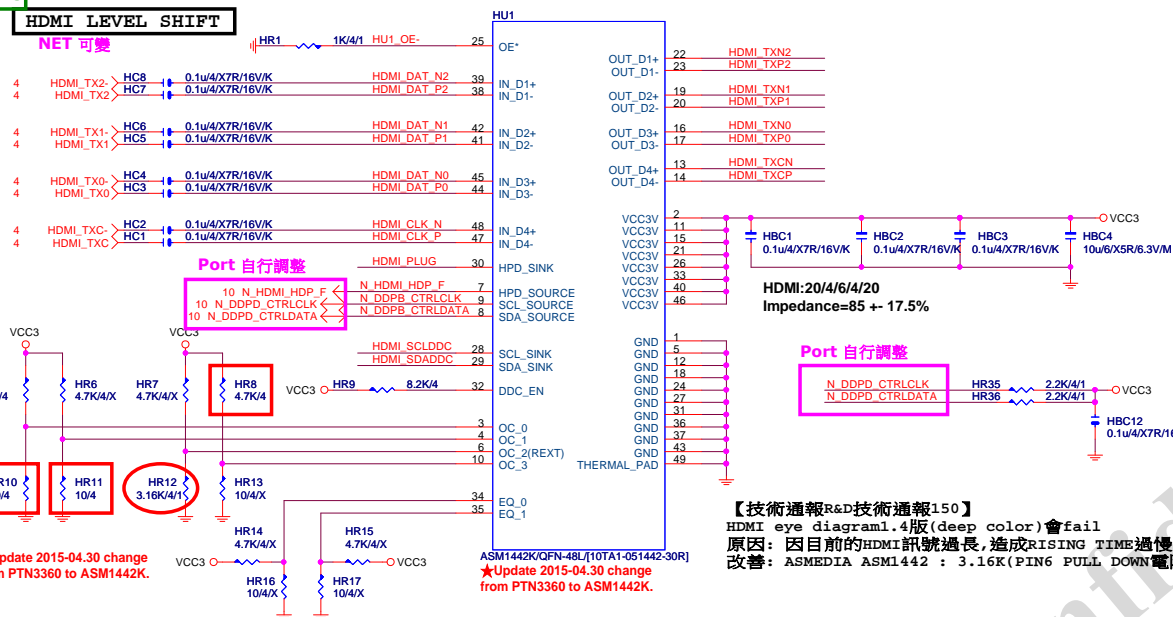
SPKR



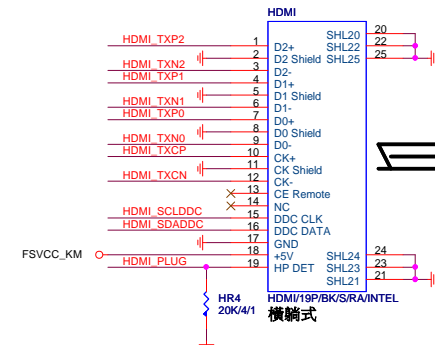
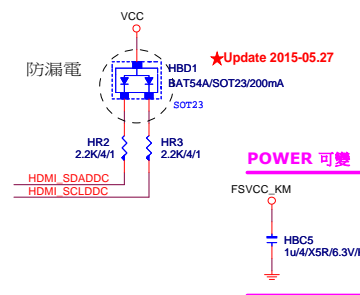
Gigabyte Confidential
Do not Copy

HDMI LEVEL SHIFT

NET 可變



PTN3360:PIN 4/10/34/35 NC PIN,都不上值;只上HR12:10K
ASM1442:紅色框要上,HR12:3.16K



【技術通報R&D技術通報150】

HDMI eye diagram1.4版(deep color)會fail

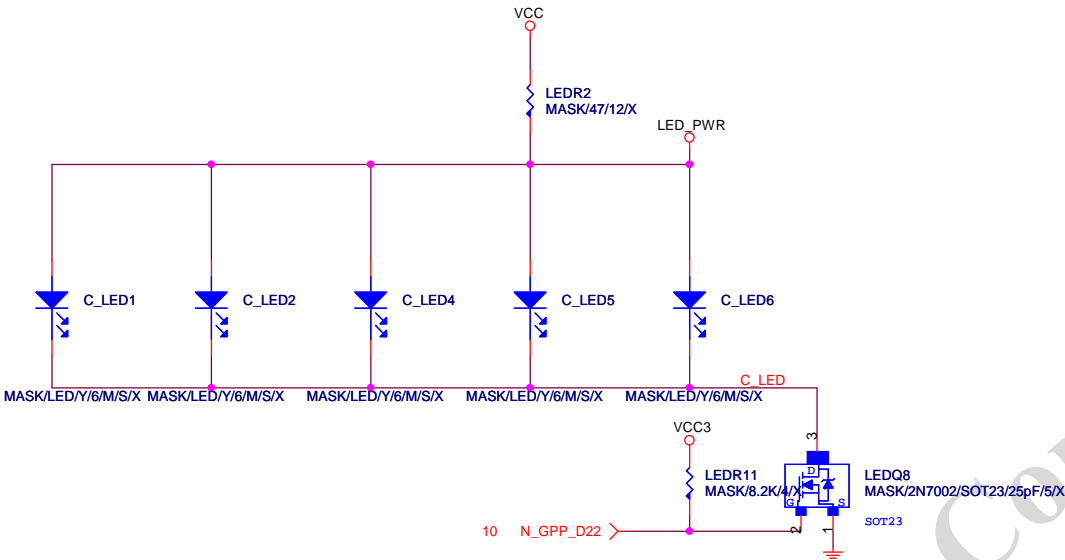
原因：因目前的HDMI訊號過長，造成RISING TIME過慢，而會壓到eye diagram

改善: ASMEDIA ASM1442 : 3.16K(PIN6 PULL DOWN電阻) 10ohm(PIN4 PULL DOWN電阻)

Rev 2.02

Ambient LED Control

	N_GPP_D22
Fill Mode	H
OFF Mode	L



GIGABYTE™

Title			Amient Single LED	
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CLOSE SIO

EMIC1
100p/4/NPO/50V/J/X

12,16,27 N_SLP_S3 ←

CLOSE PCH

EMIC4
100p/4/NPO/50V/J/X

4,12 N_CPUPWROK ←

close to PCH (NR17)

R_USB30_2 & DVI 之間

close to SIO (PIN92) MPD-

EMIC6
0.01u/4/X7R/25V/K

16,4 MPD- ←

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Title

EMI/ESDSize
A

Document Number

H310M H

Rev

1.01

Date: Tuesday, February 27, 2018

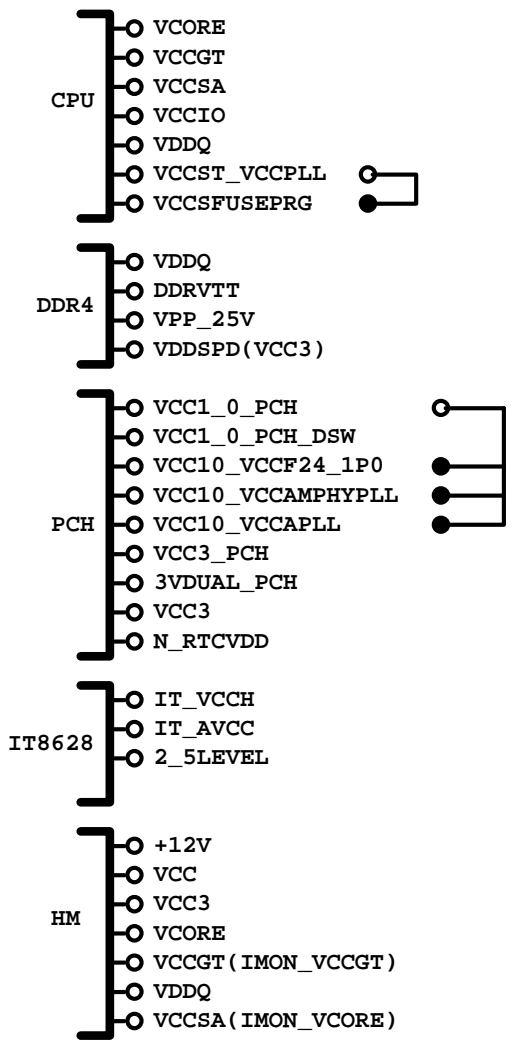
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47

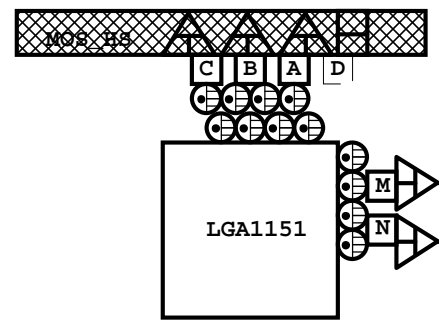
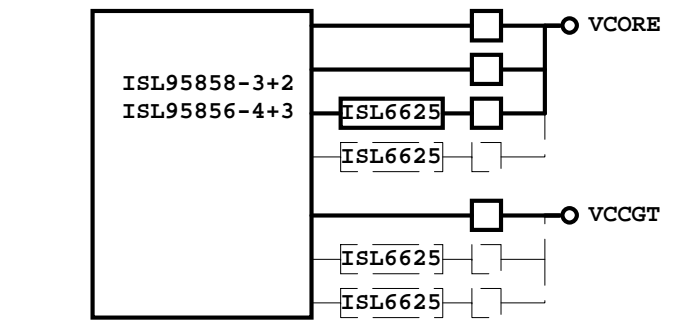
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50

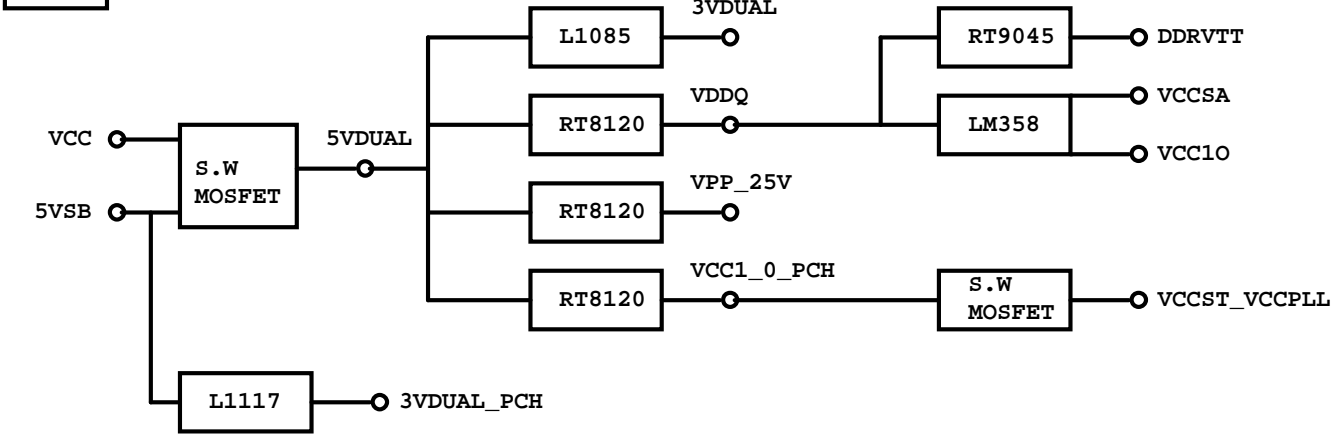
POWER BLOCK MAP



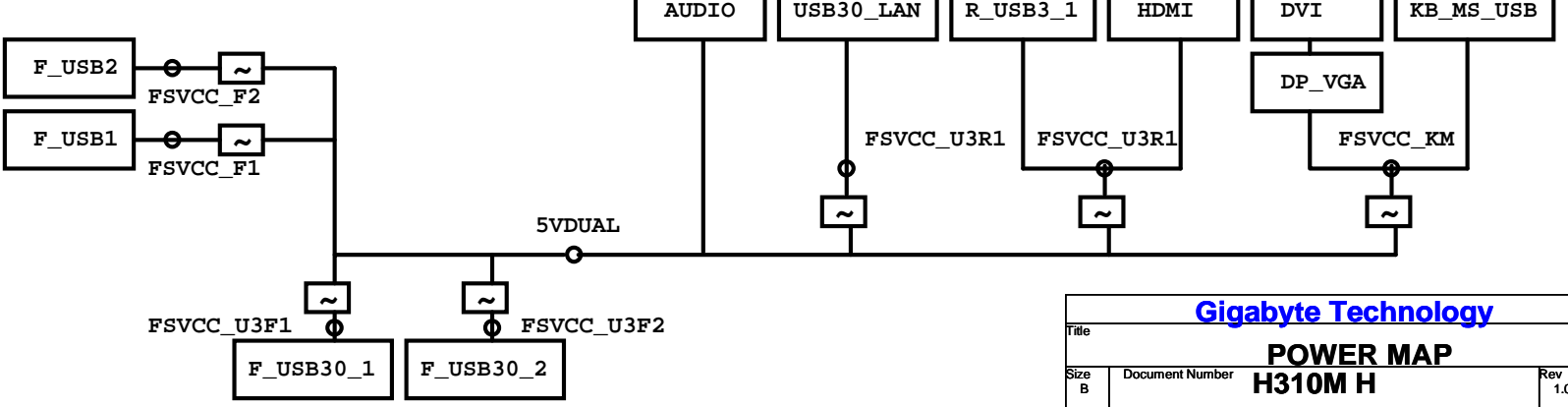
VCORE/VCCGT



POWER



FUSE POWER F/R



固態電容料號.請自行修改

日系黑色固態	Capture Value
11C02-C85600-01R	560u/FP/D/6.3V/68/C/8m
11C05-C82700-01R	270u/FP/D/16V/88/C/12m
11C05-C61000-01R	100u/OS/D/16V/66/C/30m
11C02-C51000-01R	100u/FP/D/6.3V/65/C/13m

日系一般固態	Capture Value
11CO2-685600-01R	560u/FP/D/6.3V/68/8m
11CO5-882700-01R	270u/FP/D/16V/88/12m
11CO5-661000-03R	100u/OS/D/16V/66/30m
11CO2-651000-02R	100u/OS/D/6.3V/66/30m

台系固態	Capture Value
11CO2-661000-09R	100u/OS/D/6.3V/66/A/35m
11CO5-691000-09R	100u/OS/D/16V/69/A/35m
11CO5-8C2700-09R	270u/FP/D/16V/8C/A/10m
11CO2-695600-09R	560u/FP/D/6.3V/69/A/11m

IRON CHOKE

	料號	Capture Value	SIZE	Footprint
DIP	11LC5-M4500C-01R	0.5uH/40A/IMD109/M/D	10*10	CHOKE05U-40A-1PQ-3
DIP	11LC5-M2500C-01R	0.5uH/20A/IMD0809/M/D	8*8	CHOKE1U-R50M-IF

Ferrite

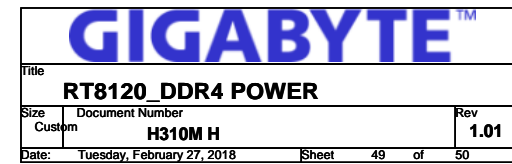
	料號	Capture Value	SIZE	Footprint
DIP	11LC5-F3500C-11R	0.5uH/32A/INCG109/FSI/D	10*10	CHOKE05U-40A-1PQ-3
DIP	11LC5-F2500C-11R	0.5uH/25A/INC0809/F/D	8*8	CHOKE1U-R50M-IF
SMD	未建(SIUC1007-R30M-JJ1W)		10*7	CHOKE11X8MM-SMD

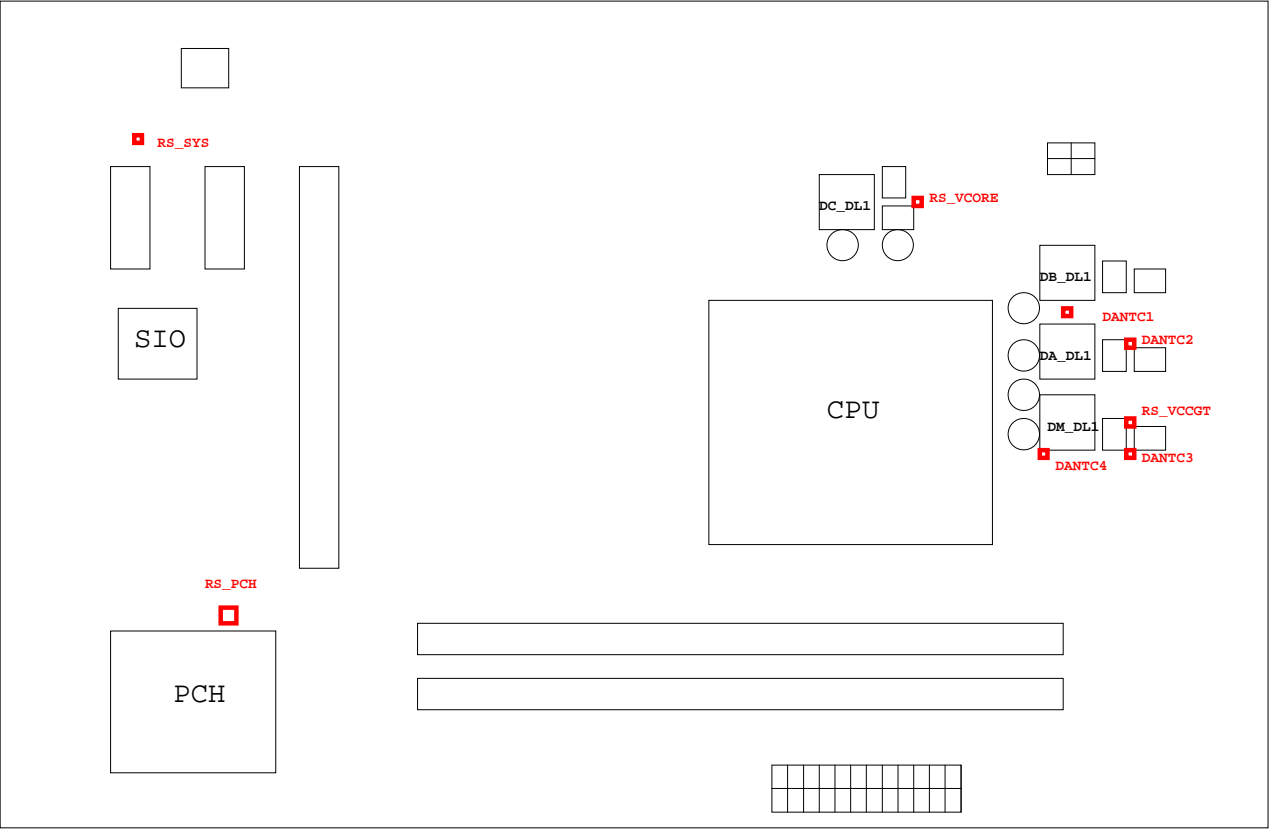
BEAD

	料號	Capture Value	SIZE	Footprint
DIP	10LFB-15470A-01R	47/4030/15A/S	4*3	BEADC8B-BPH_SMD

PWM料號

		料號	Capture Value	Footprint
PWM	ISL95856	10TA1-695856-01R		IC52QFN-6x6-G
PWM	ISL95858	10TA1-695858-01R		IC52QFN-6x6-G
PWM	IR35201	10TA1-635201-00R		IC56QFN-9VRS4339
PWM	IR3570	10TA1-603570-00R		IC40MLFP-ISL95835





熱敏電阻	擺放靠近位置	走線方式
DANTC4	DM_DL1	Differential
DANTC1	DA_DL1	Differential
DANTC3	DM_DQ1	Differential
DANTC2	DA_DQ1	Differential
RS_VCORE	DA_DQ1	N/A
RS_VCCGT	DM_DQ1	N/A
RS_PCH	PCH	N/A
RS_SYS	CU1	N/A